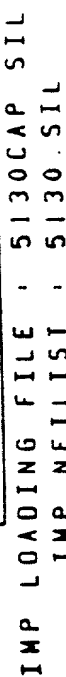


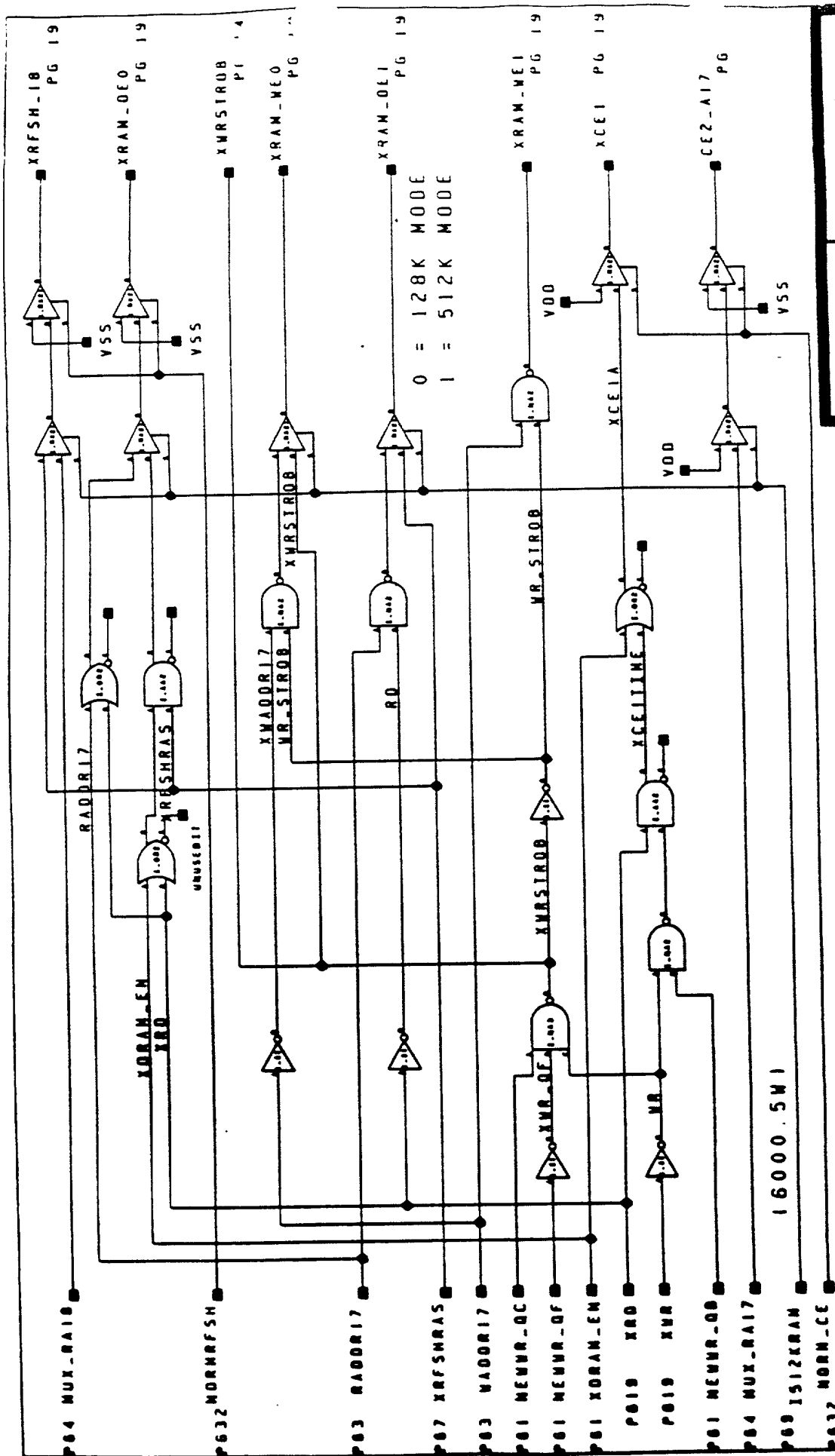
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SYSTEM AND METHOD FOR TRANSMITTING AND UTILIZING
ELECTRONIC PROGRAMS GUIDE INFORMATION

APPENDIX A

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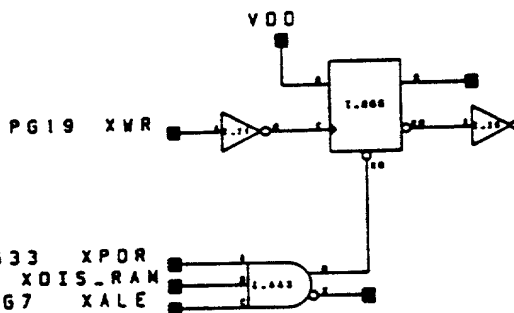




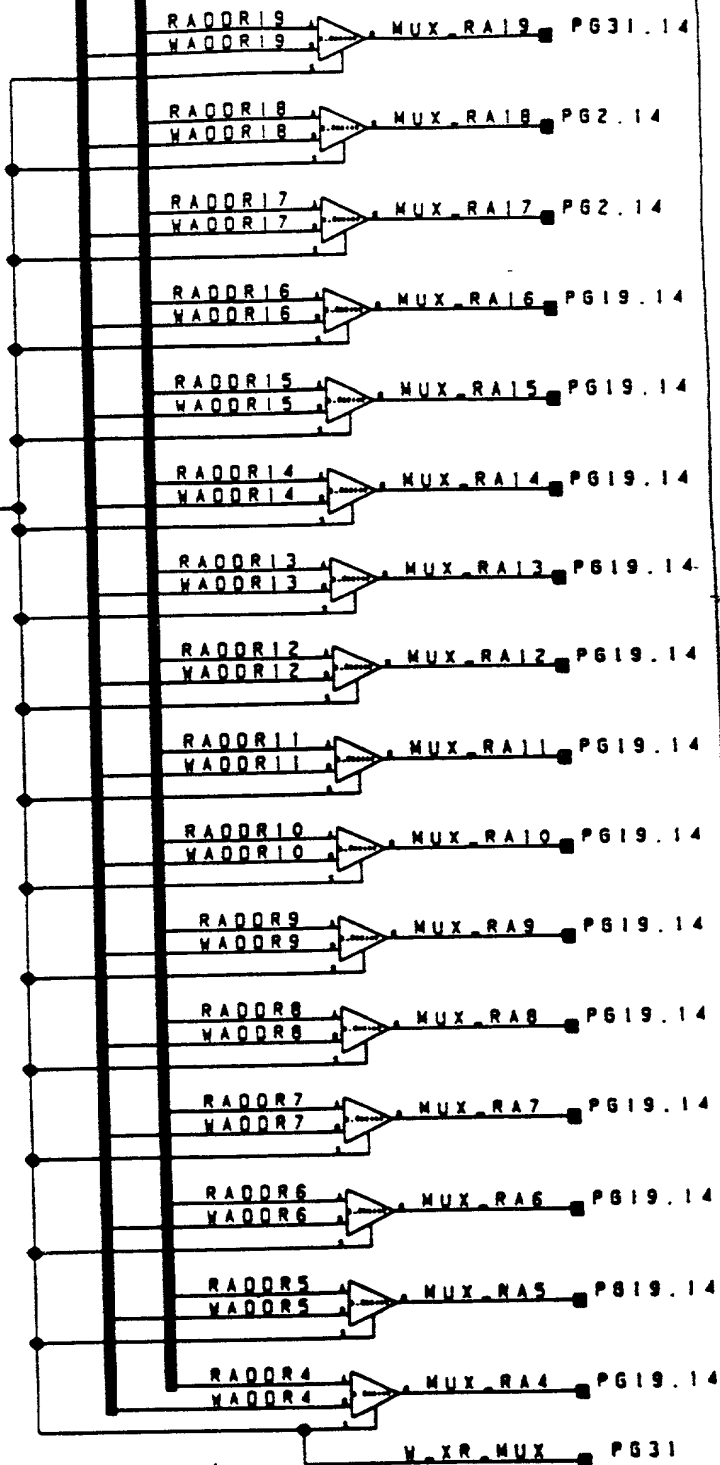
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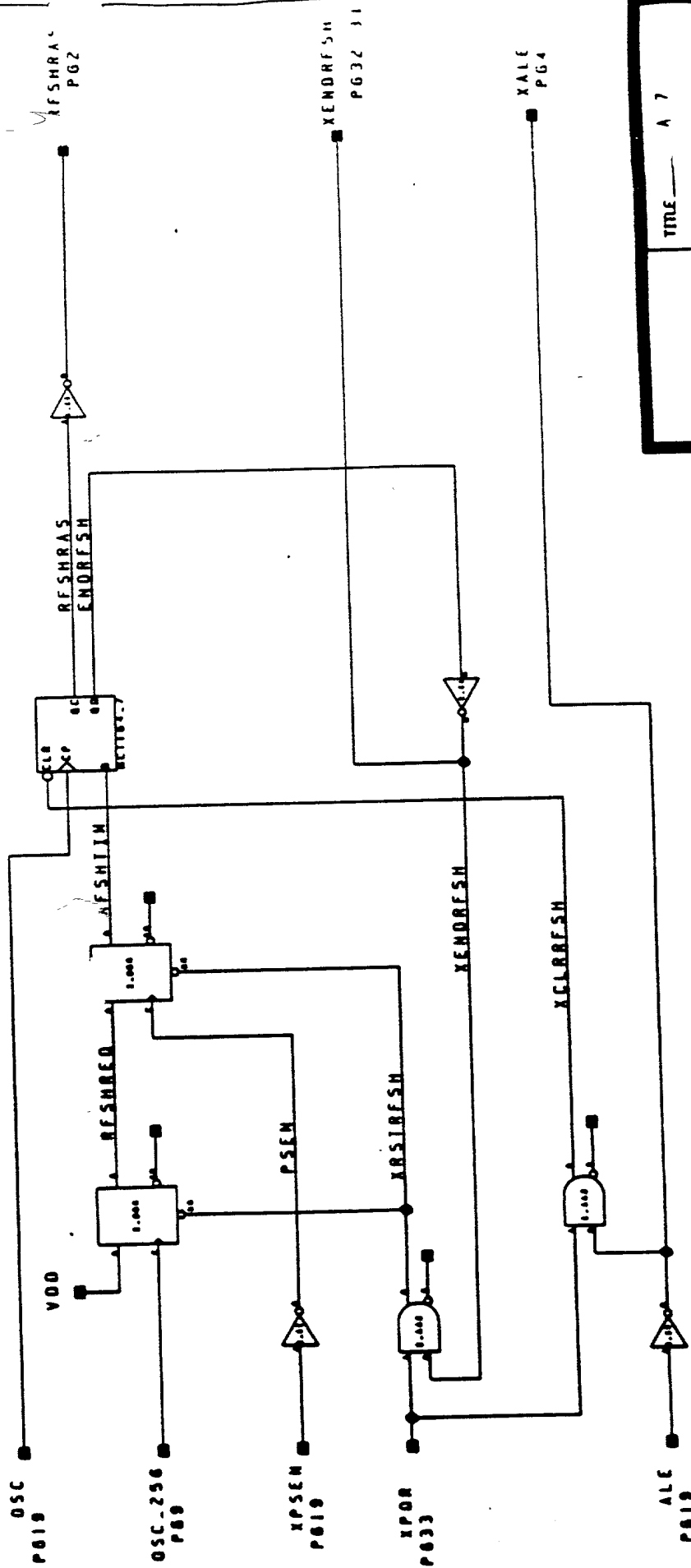
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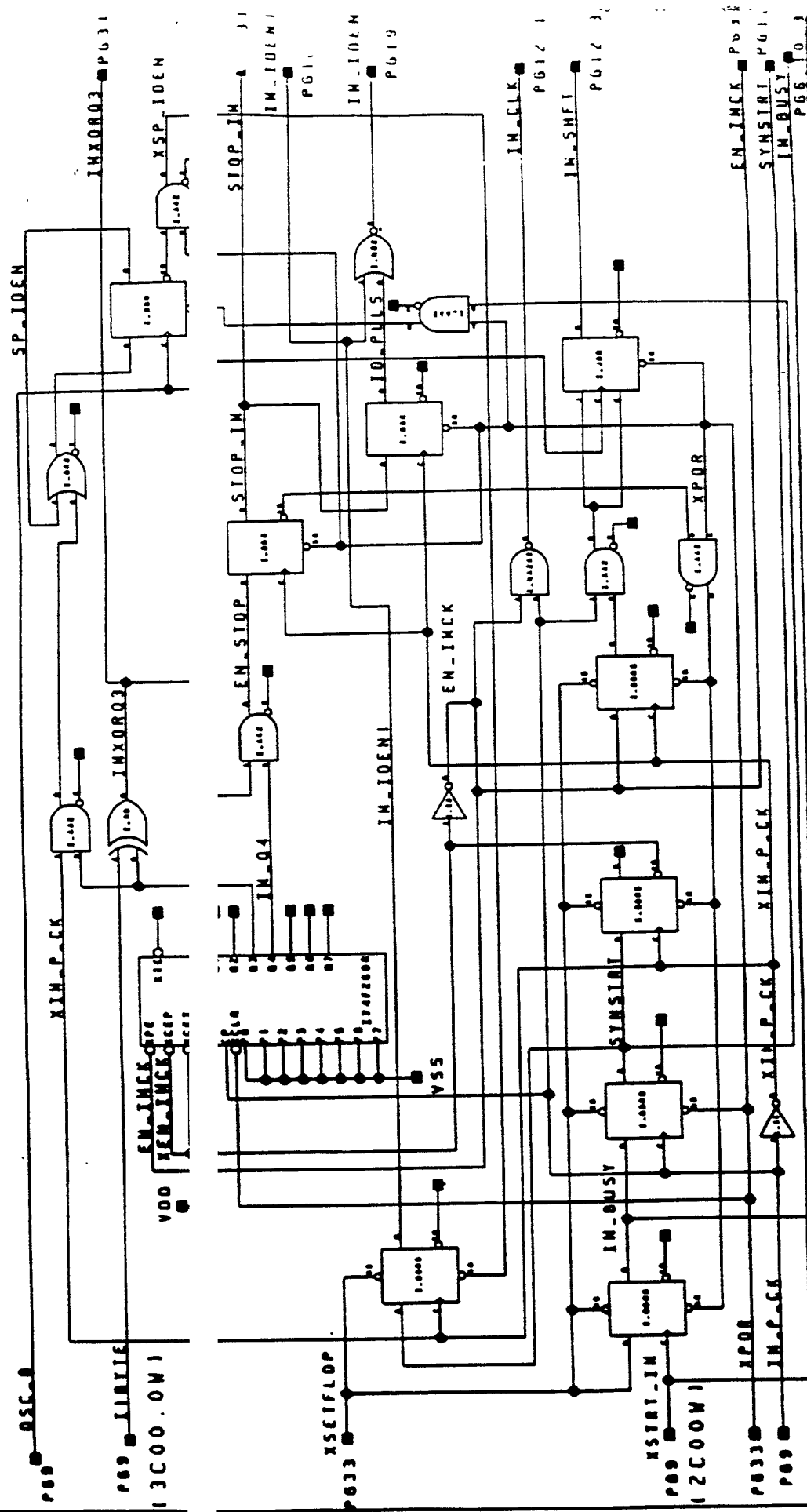
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I. M. BUS CONTROL LOGIC



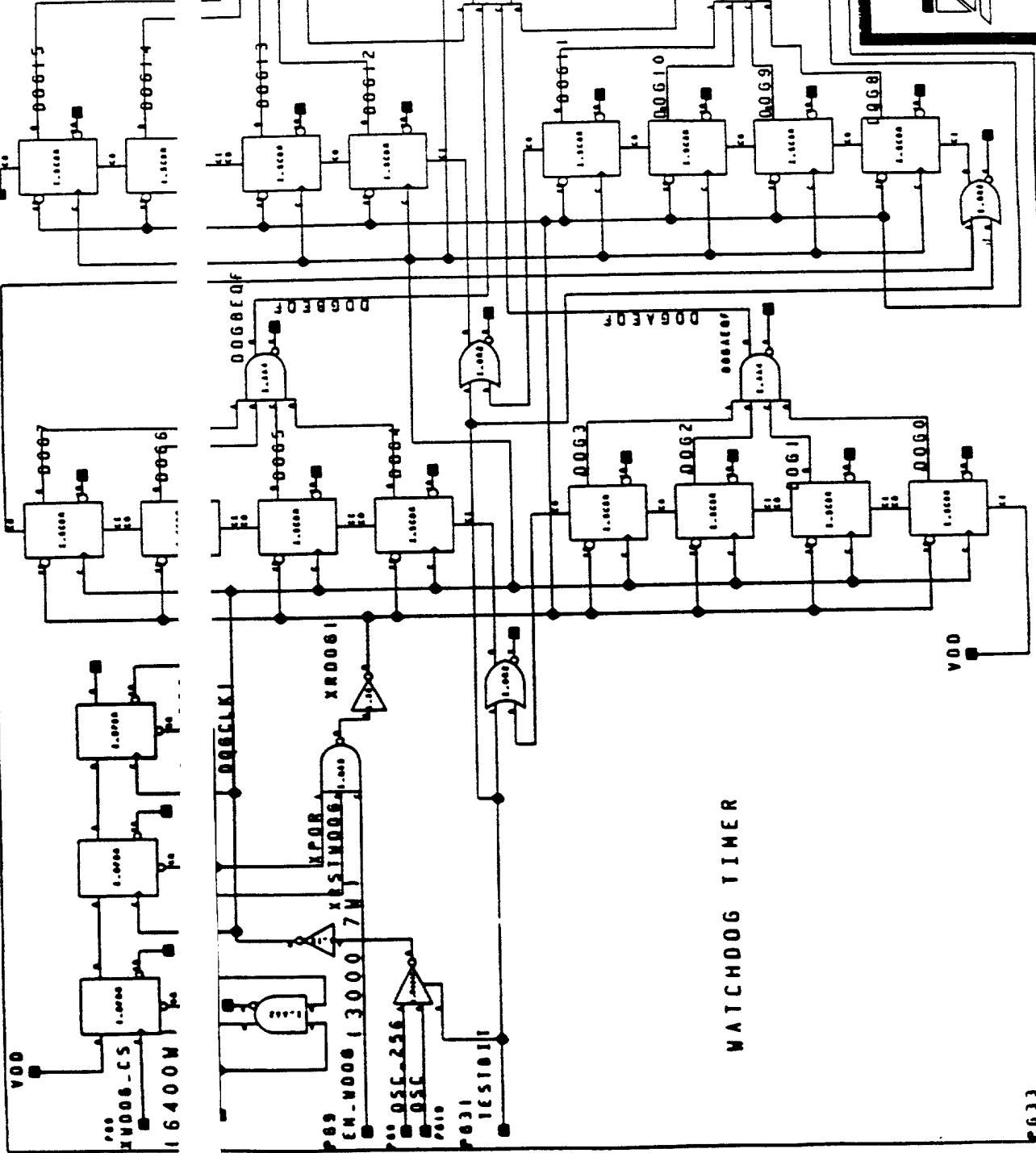
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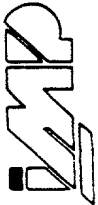
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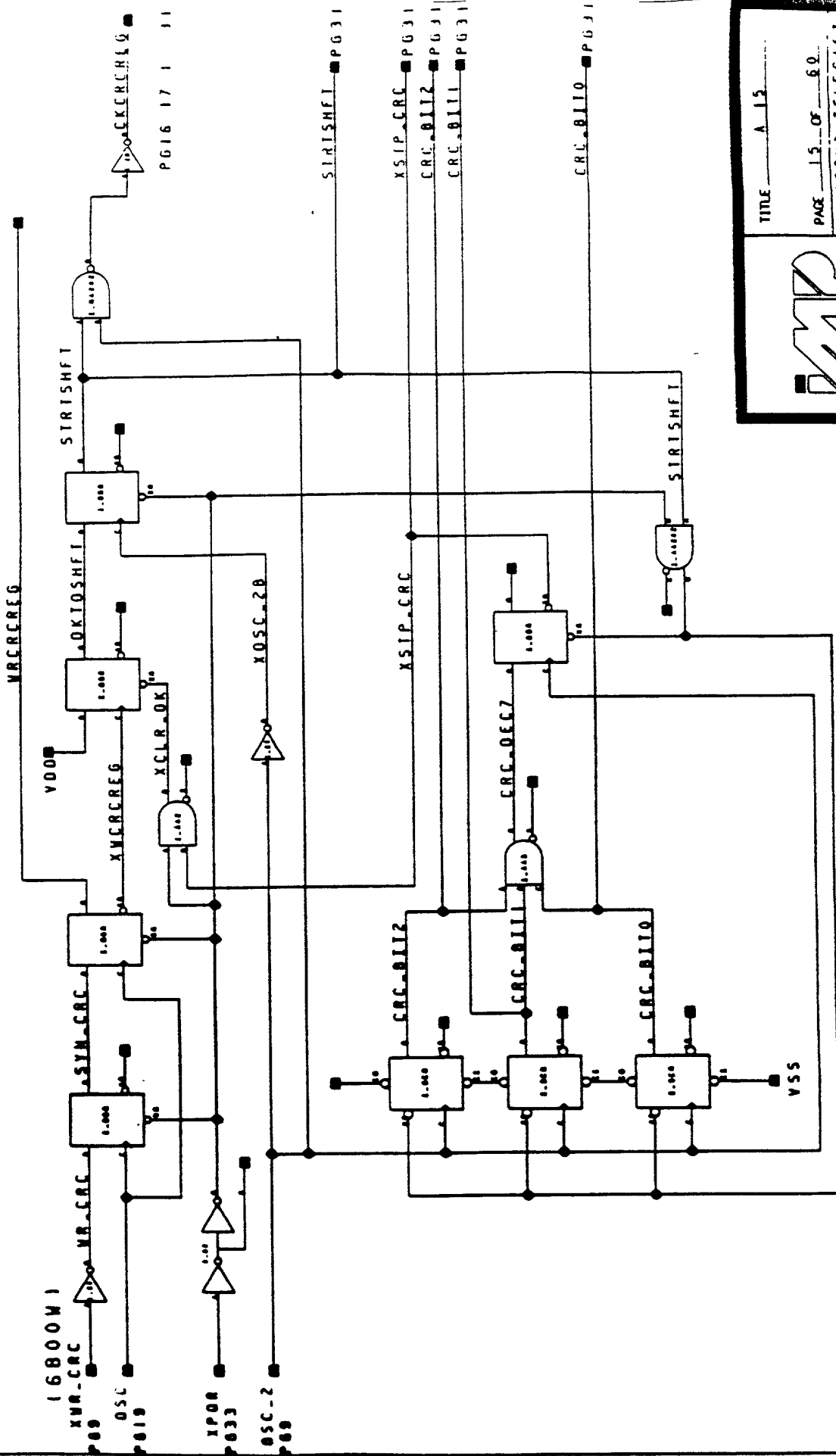
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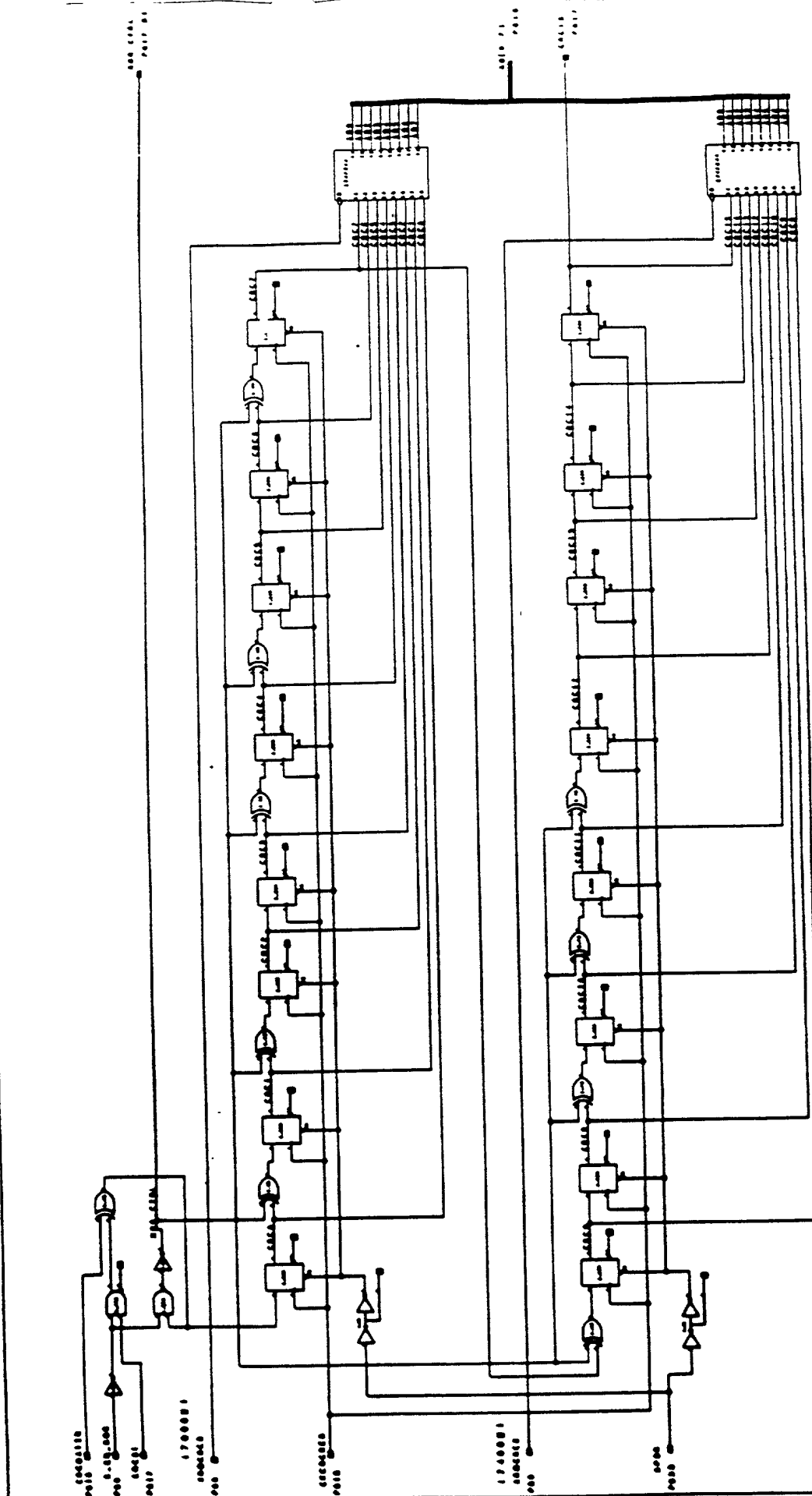
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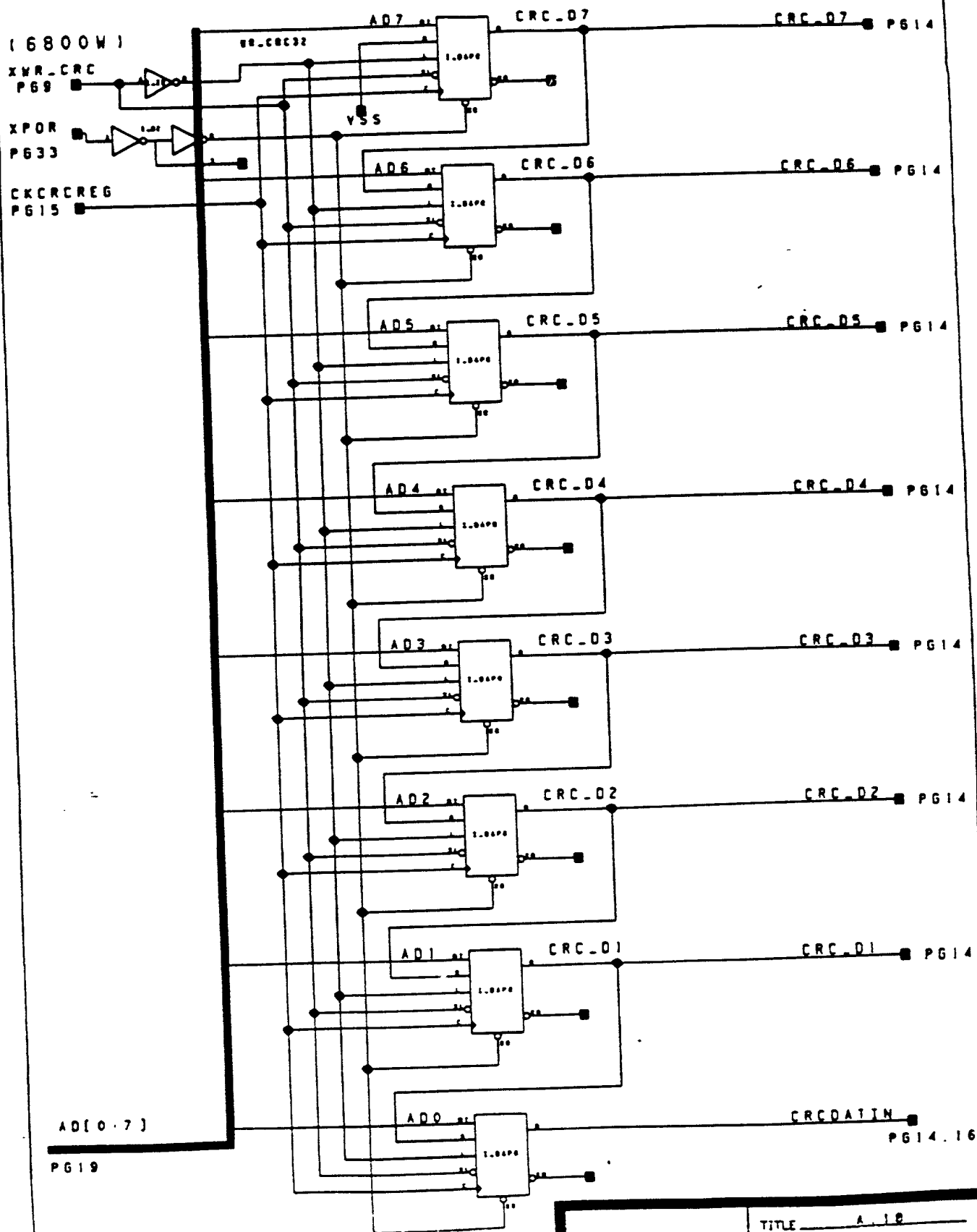
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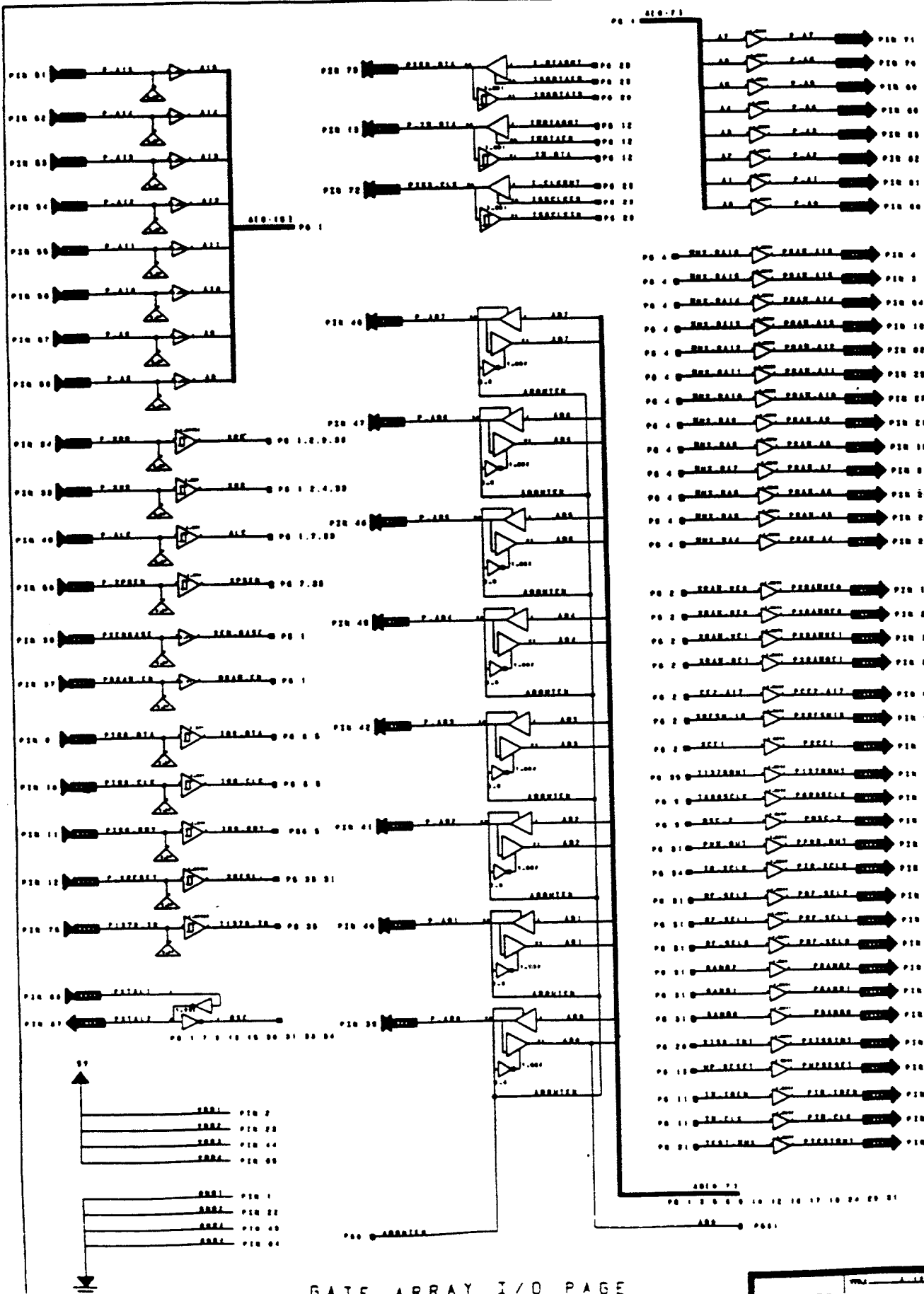
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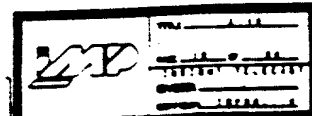


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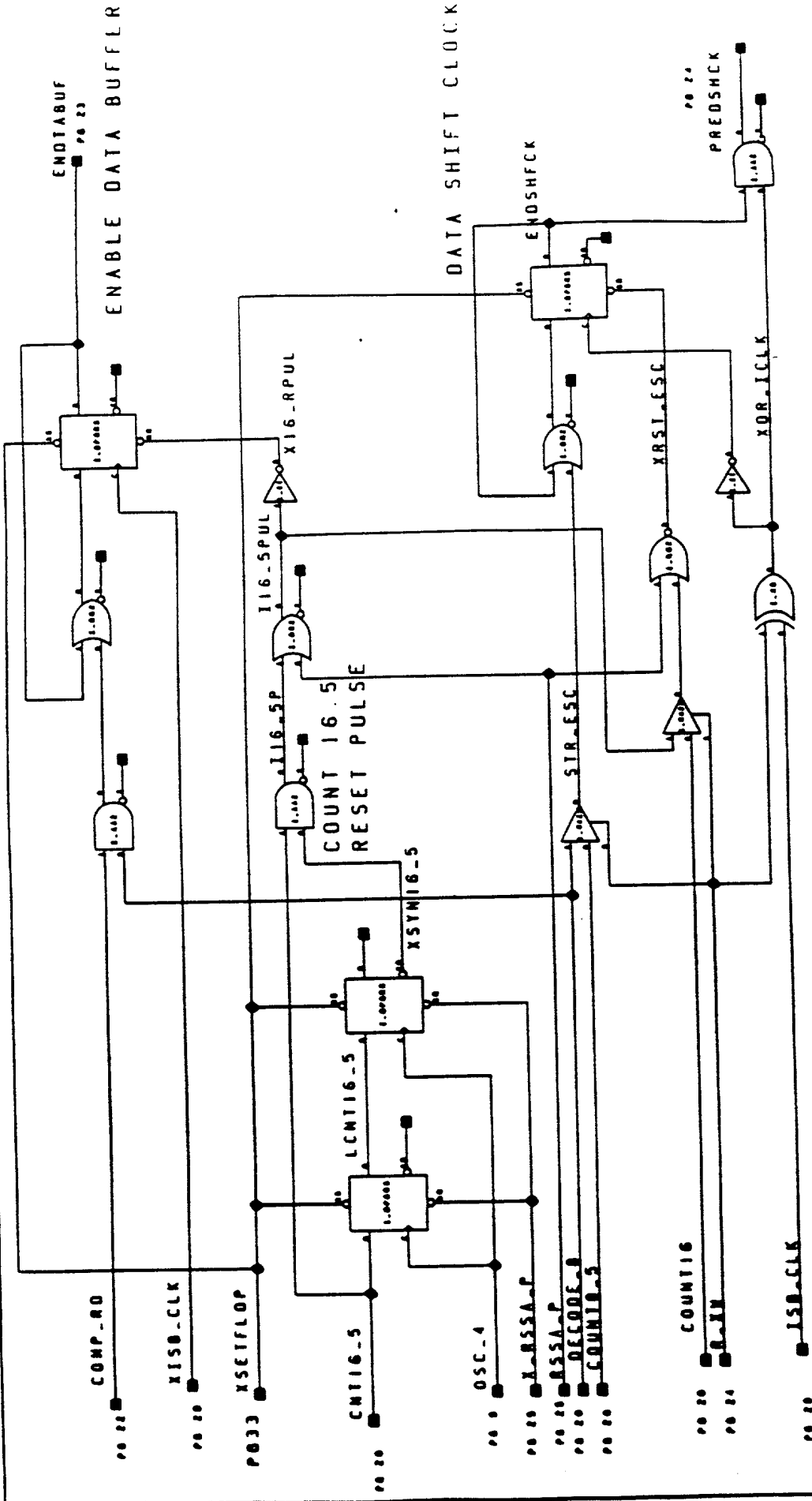
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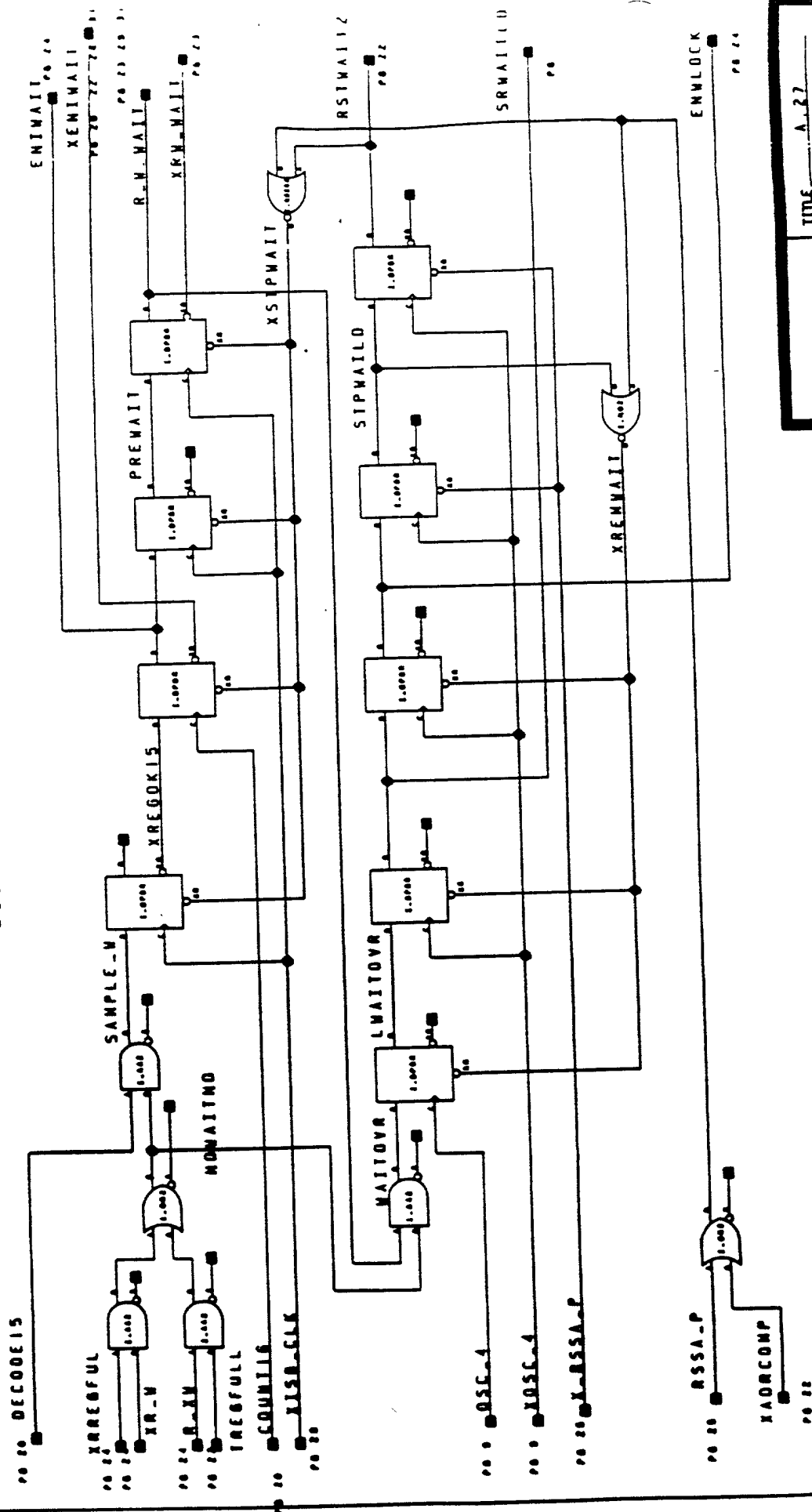
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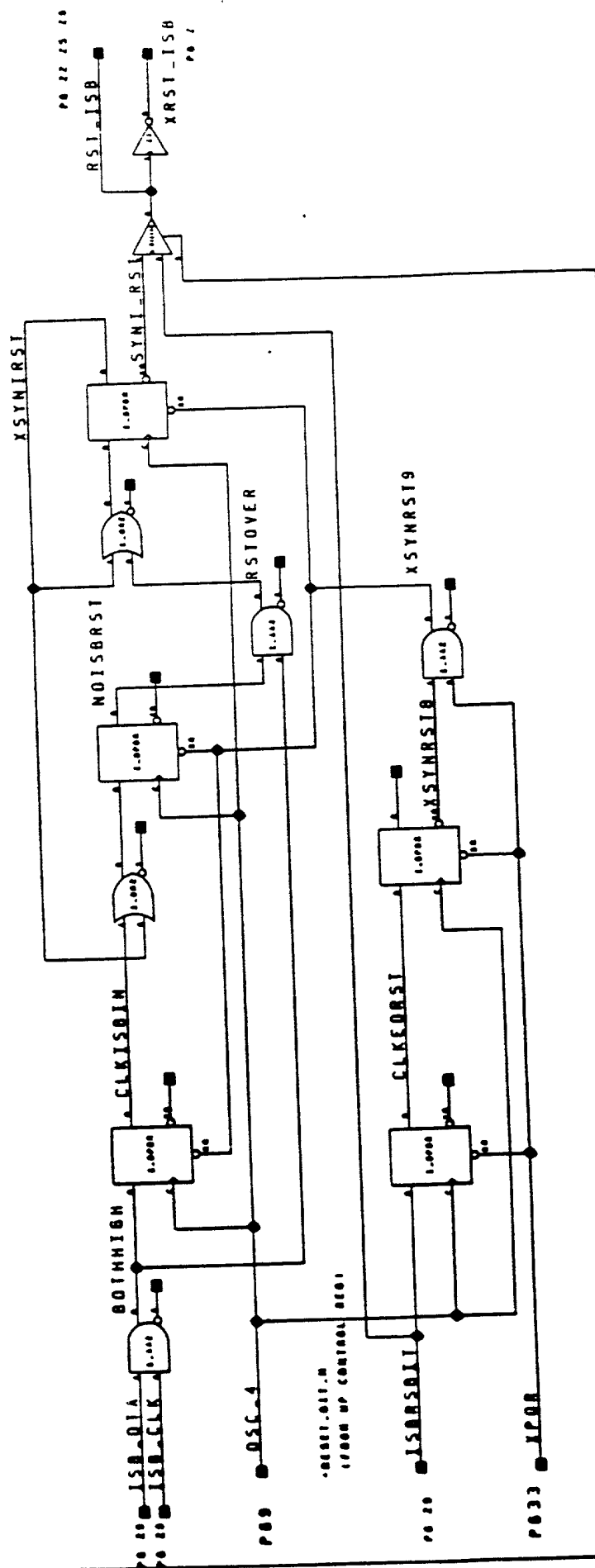
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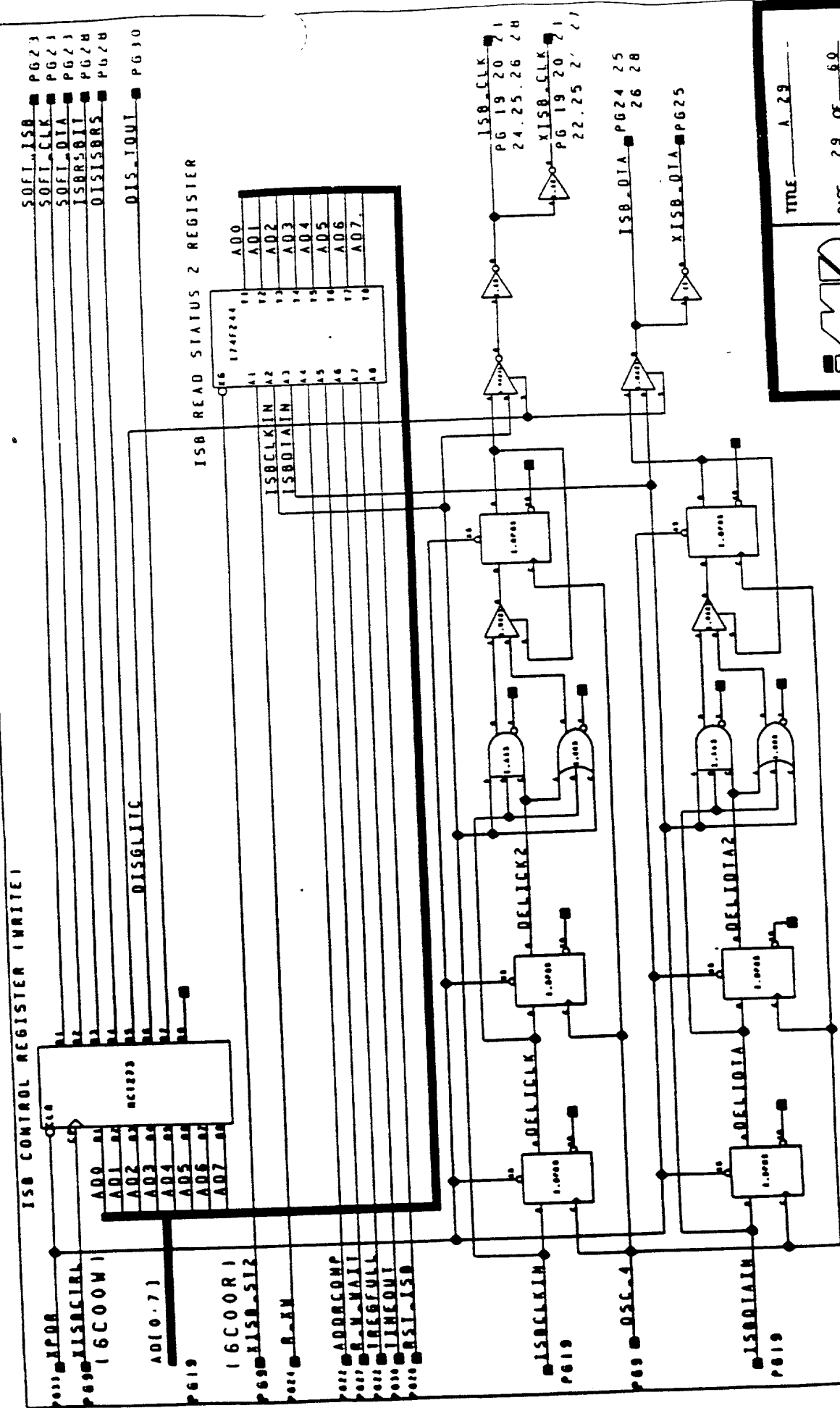


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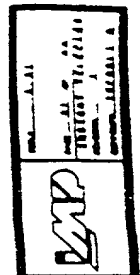
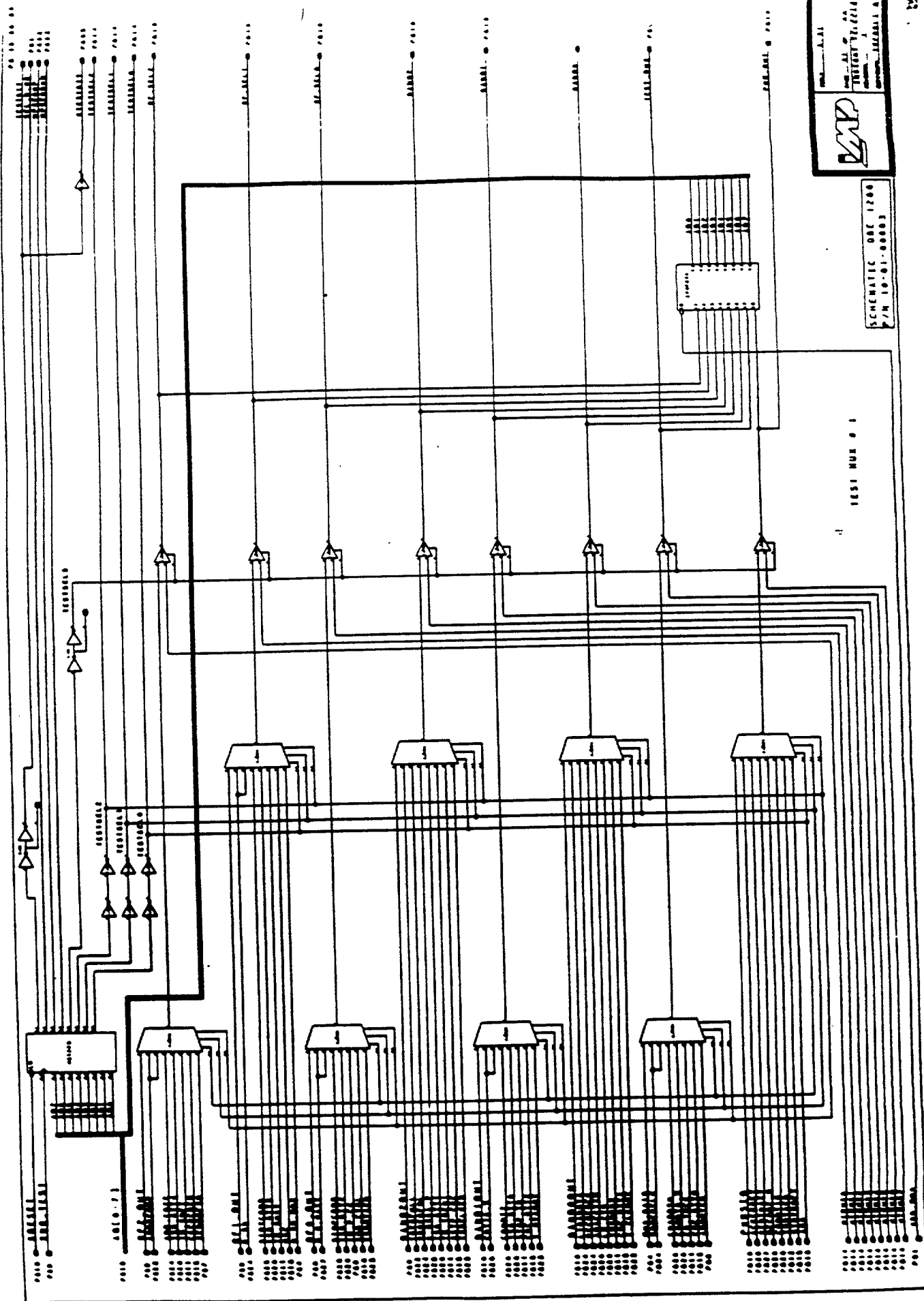


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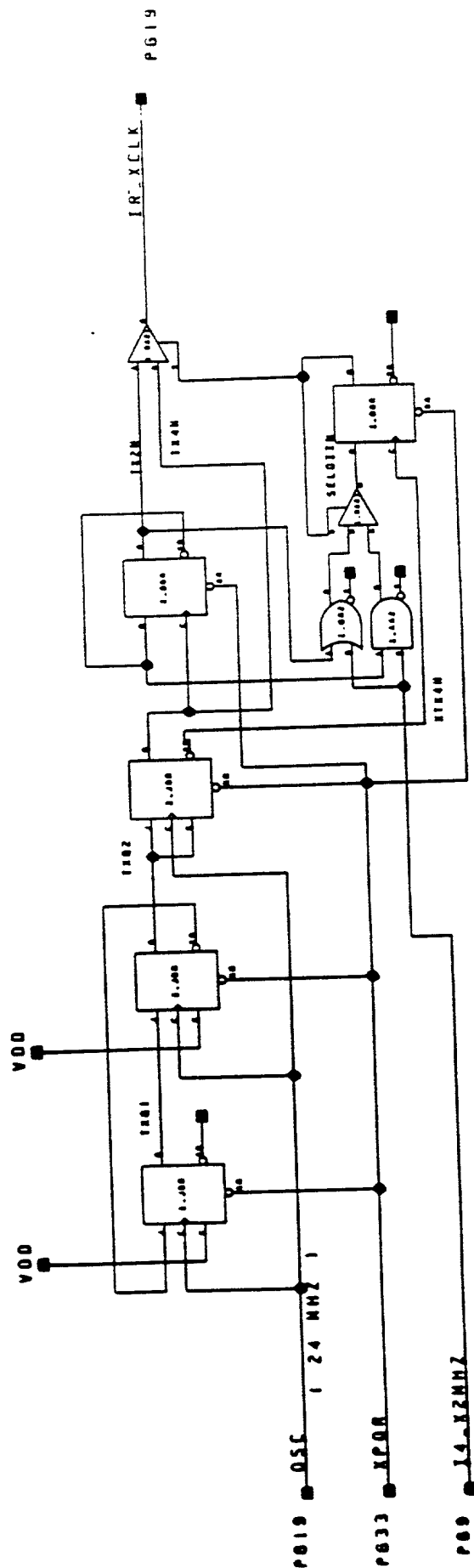
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2.2 AND 4 MHZ CLOCKS



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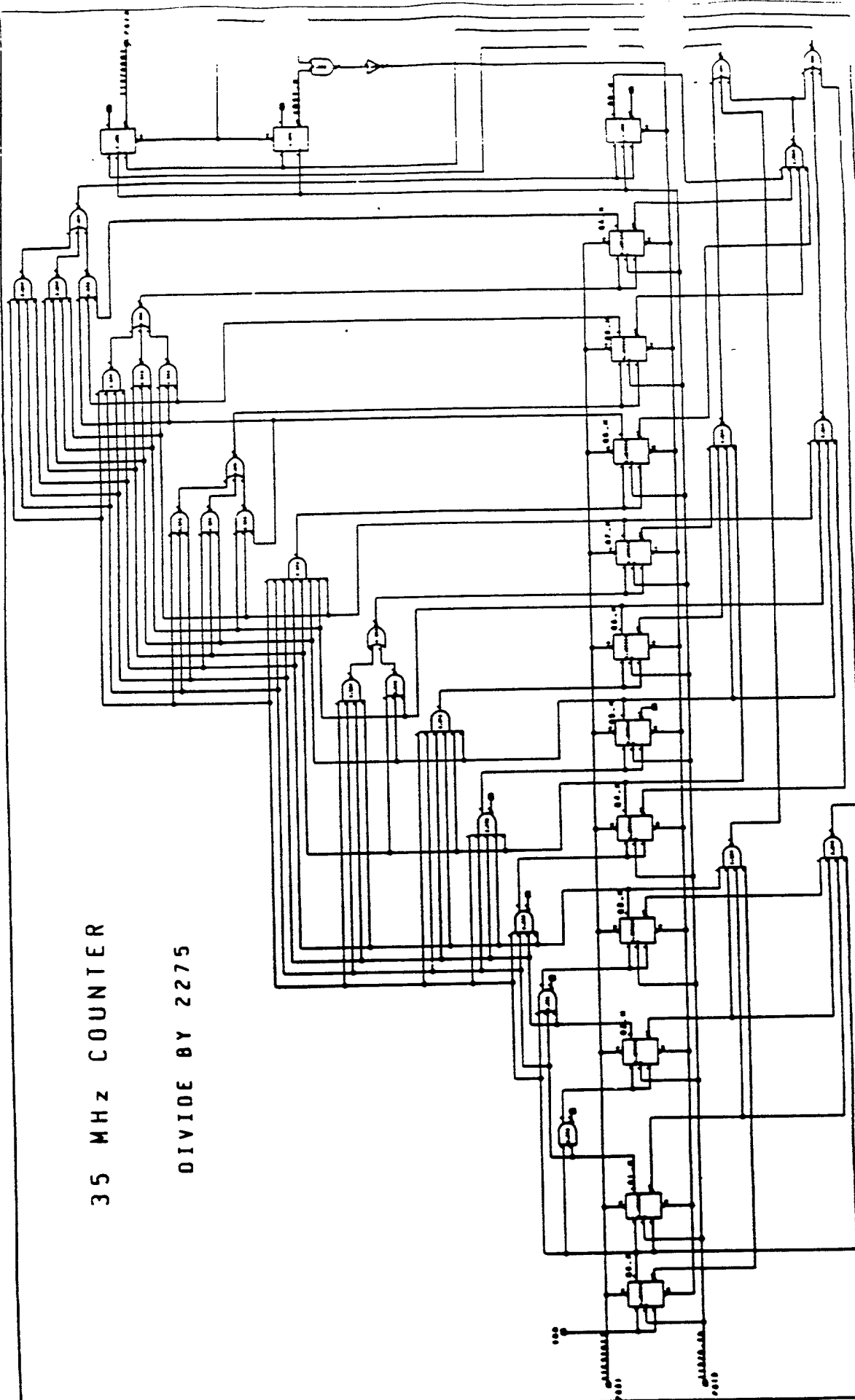
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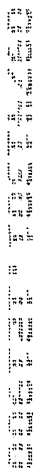
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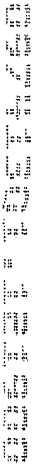
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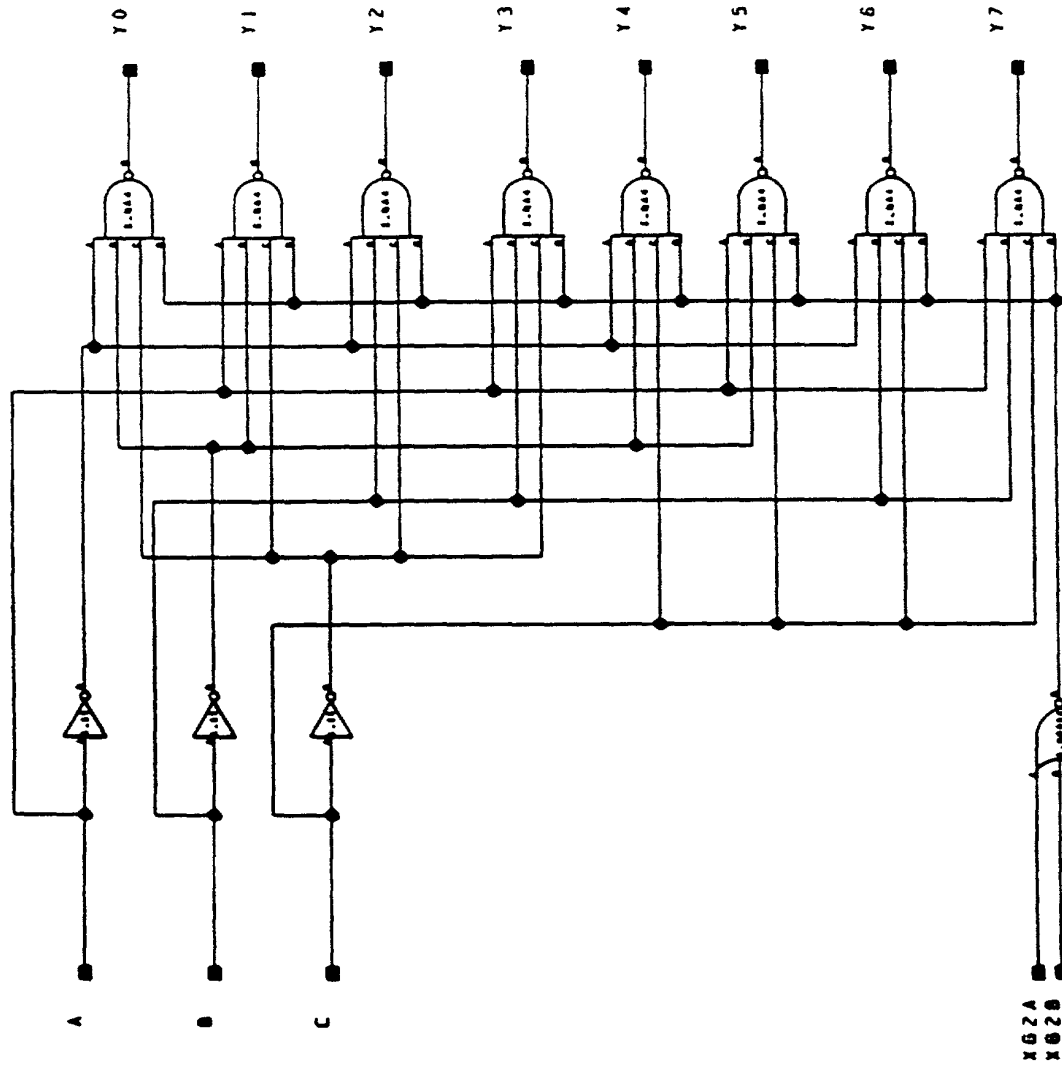
The diagram shows a three-channel signal processing system. The input signal is split into three channels: Channel 0 (top), Channel A (middle), and Channel Z (bottom). Channel 0 passes through a 1.000 gain block. Channel A passes through a 1.000 gain block and a 1.000 gain block. Channel Z passes through a 1.000 gain block and a 1.000 gain block. The outputs of all three channels are summed at a common point.

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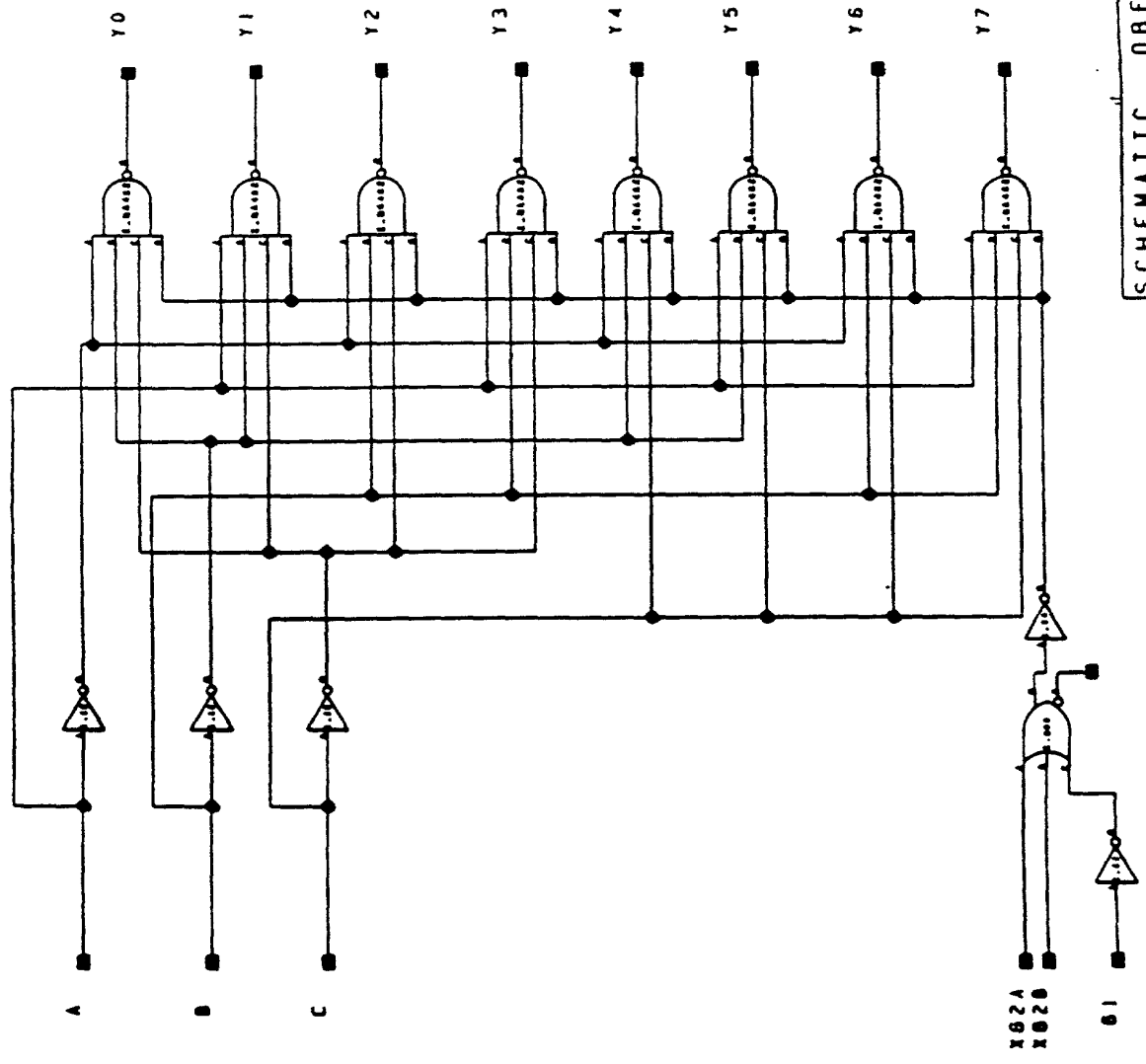


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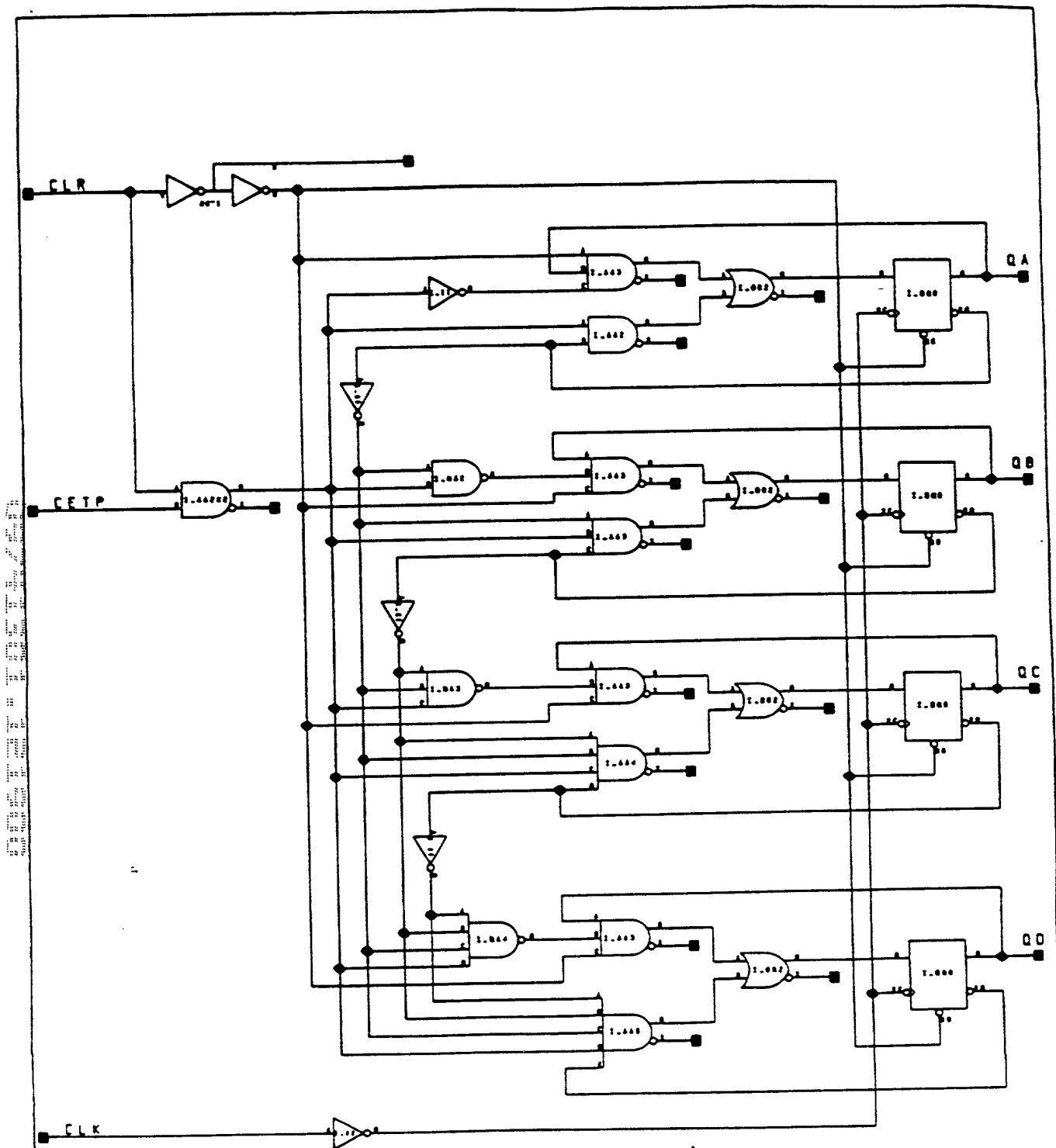
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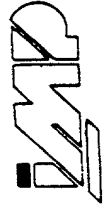
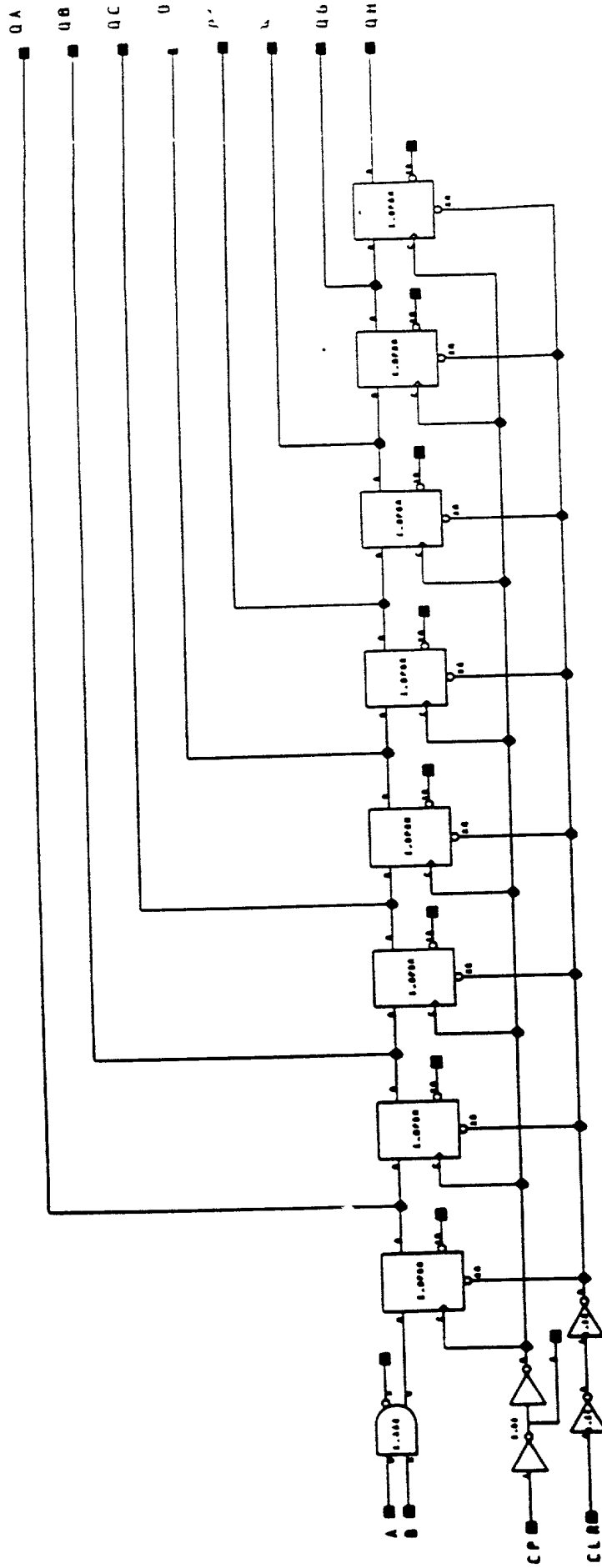
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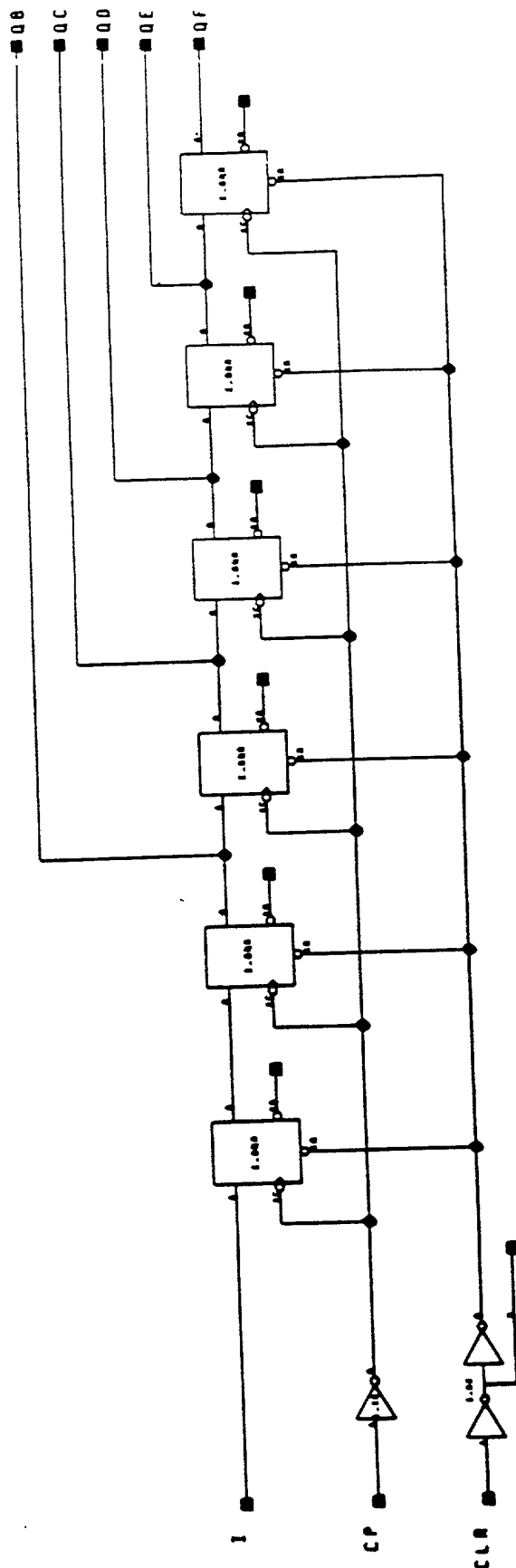
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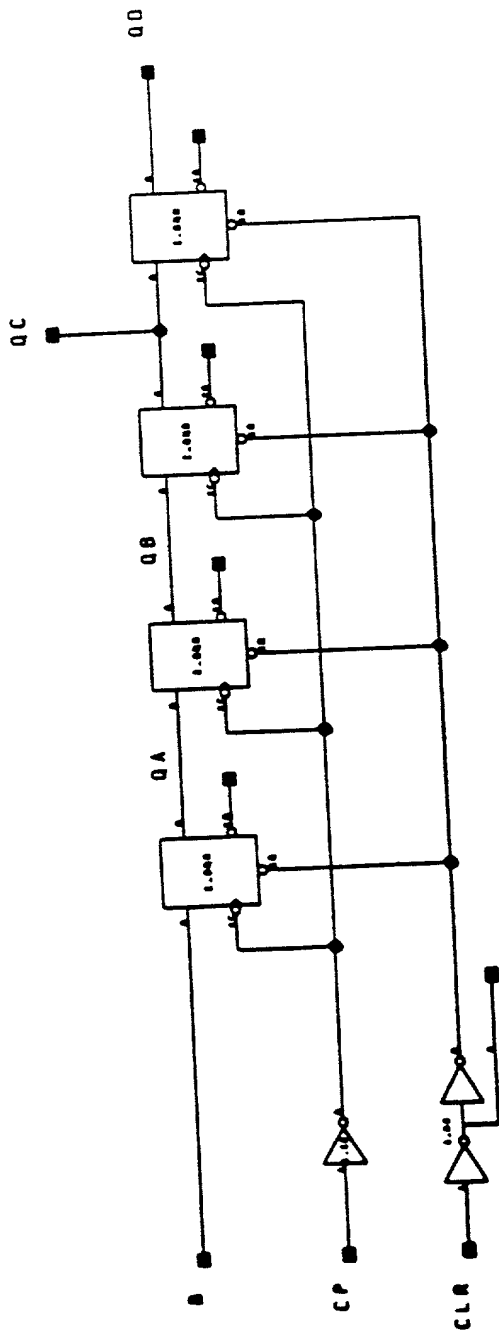


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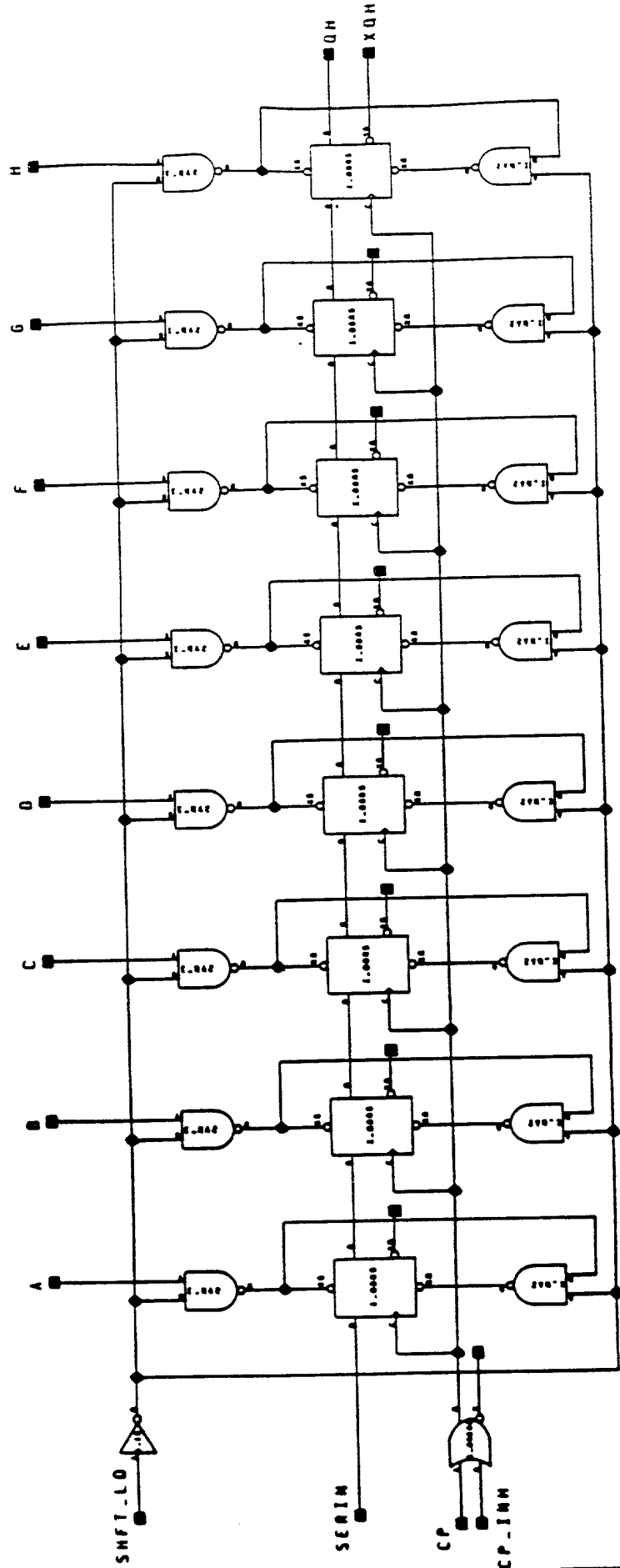
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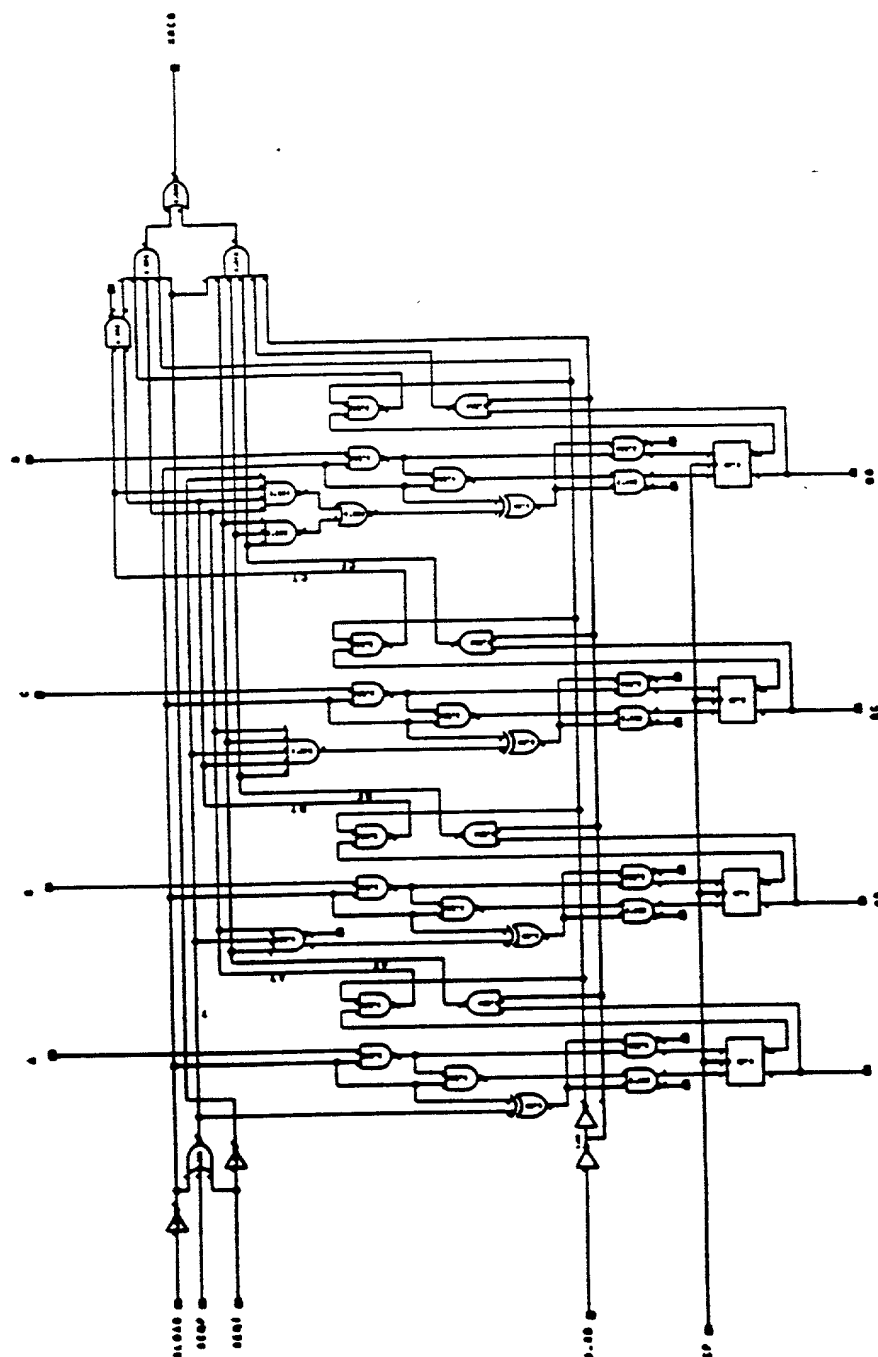
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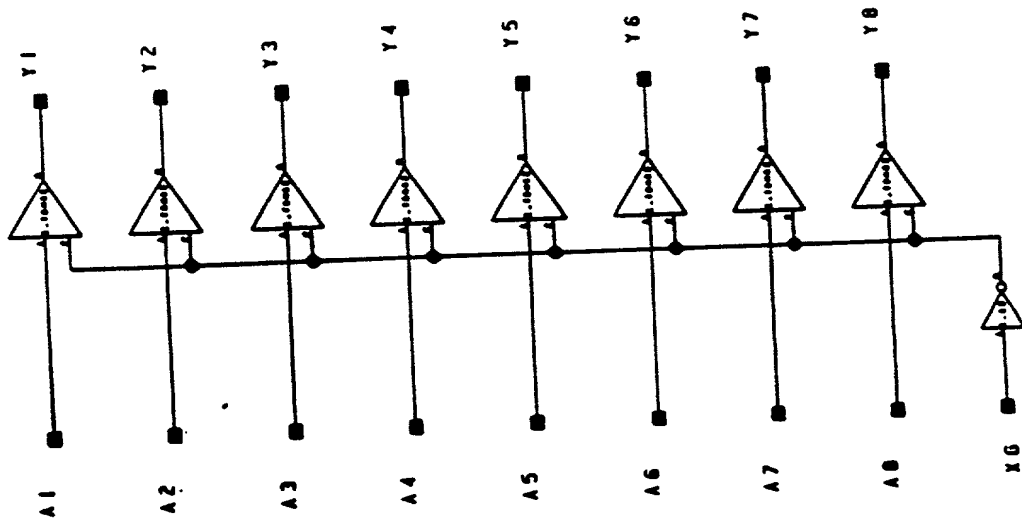
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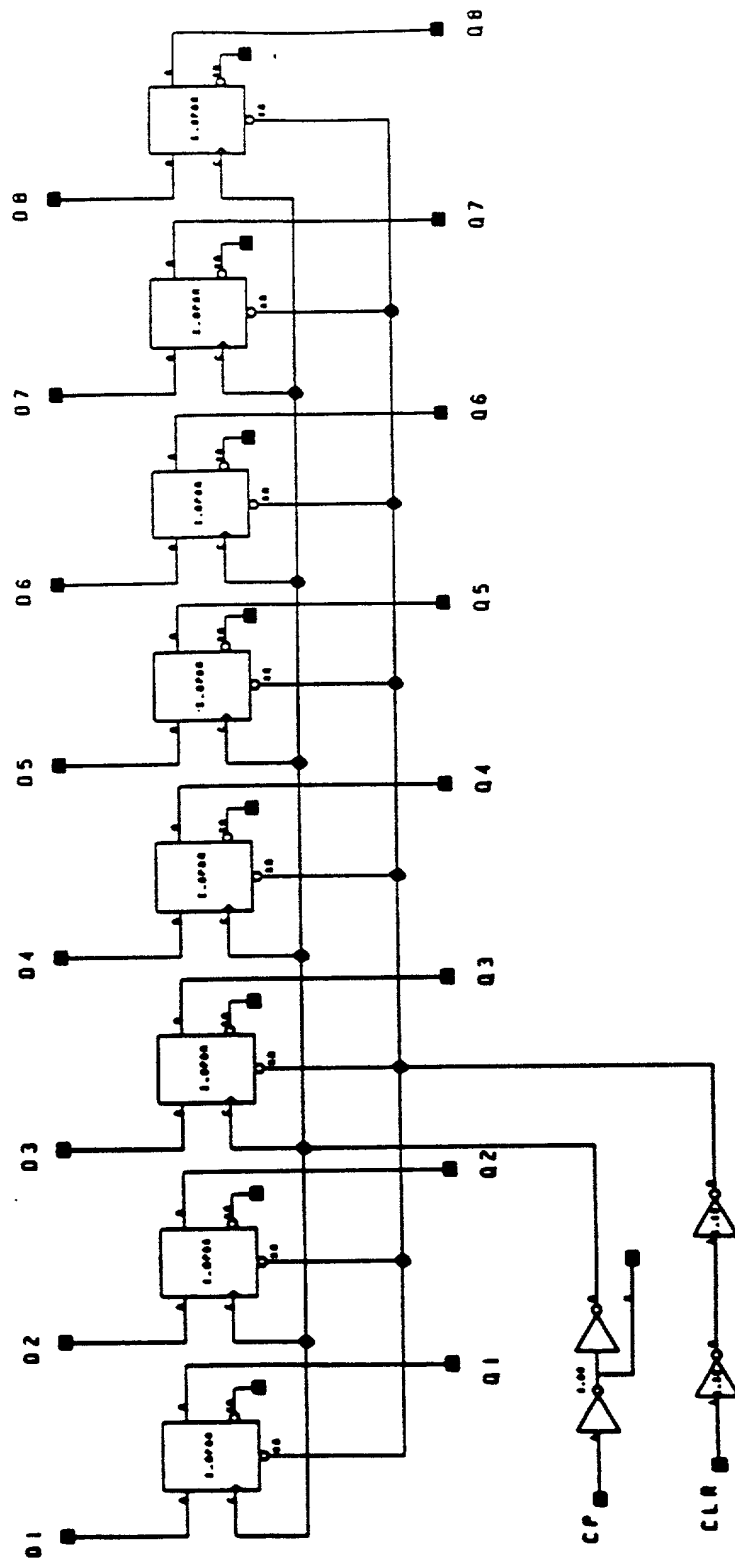
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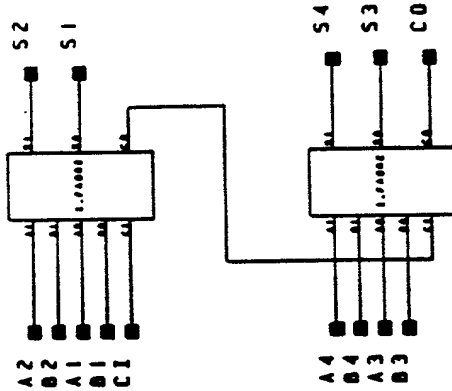
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SCHEMATIC DBE 1200
 P/N 10-01-00003

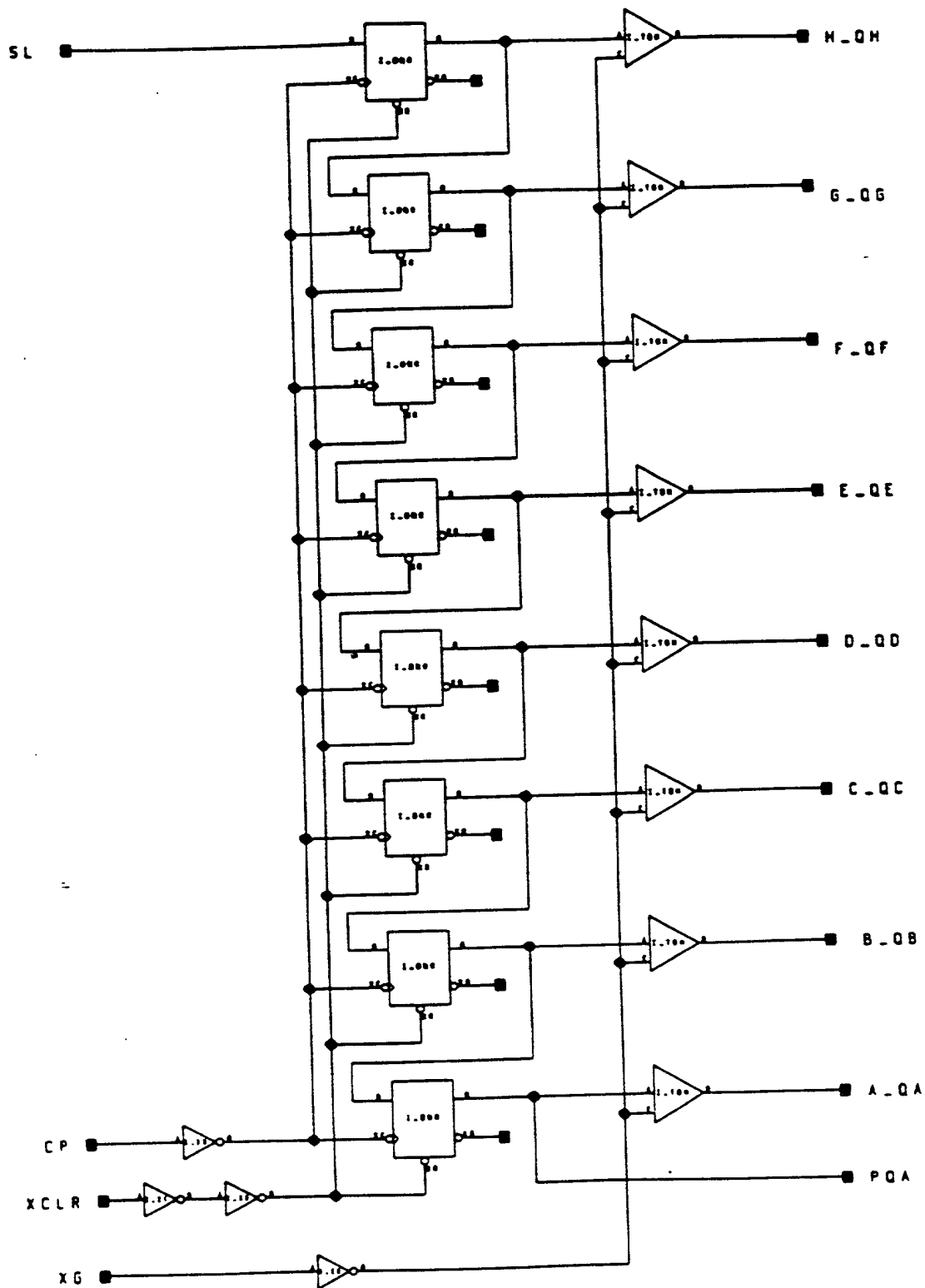
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SCHEMATIC DBE 1200
P/N 10-01-00003



TITLE 174F283
PAGE 53 OF 60
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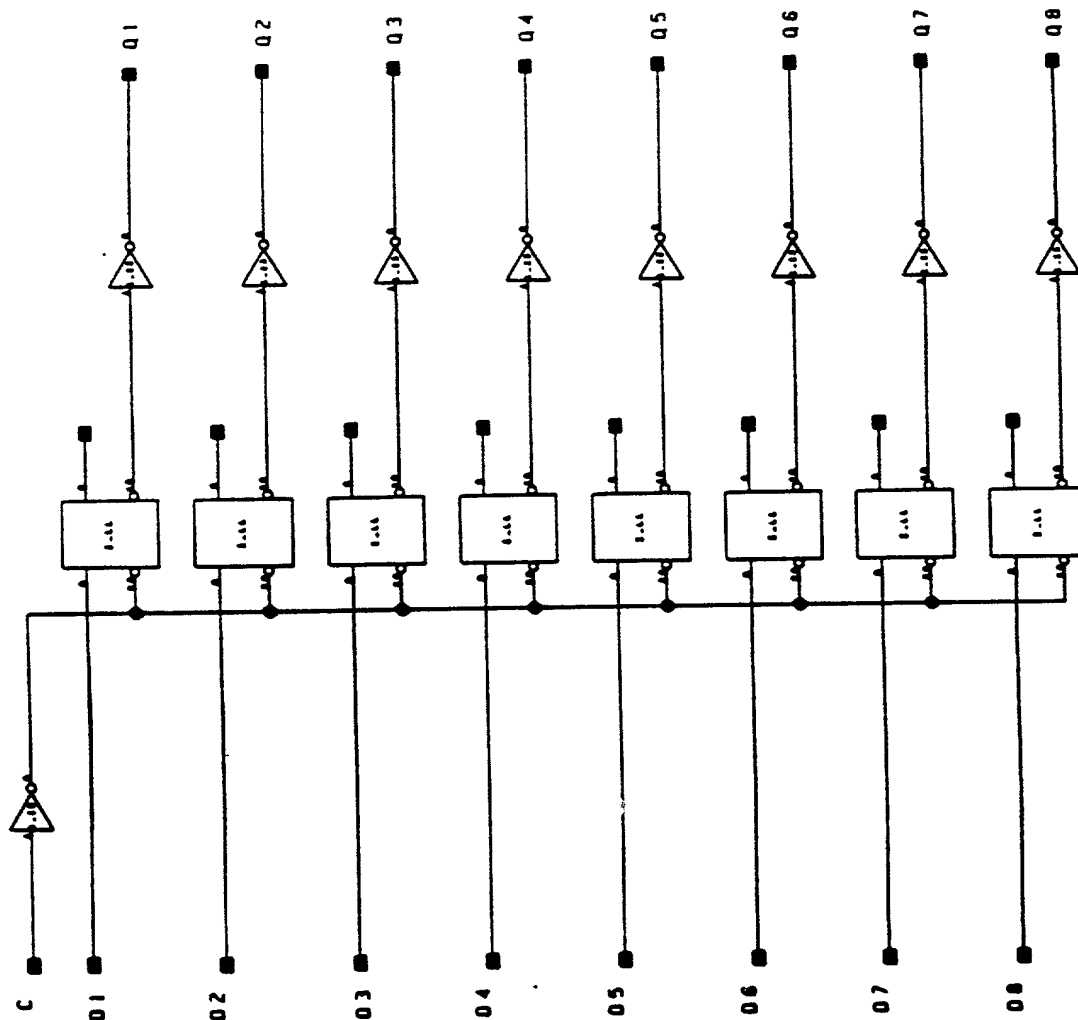


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TITLE M299-12
PAGE 54 OF 60
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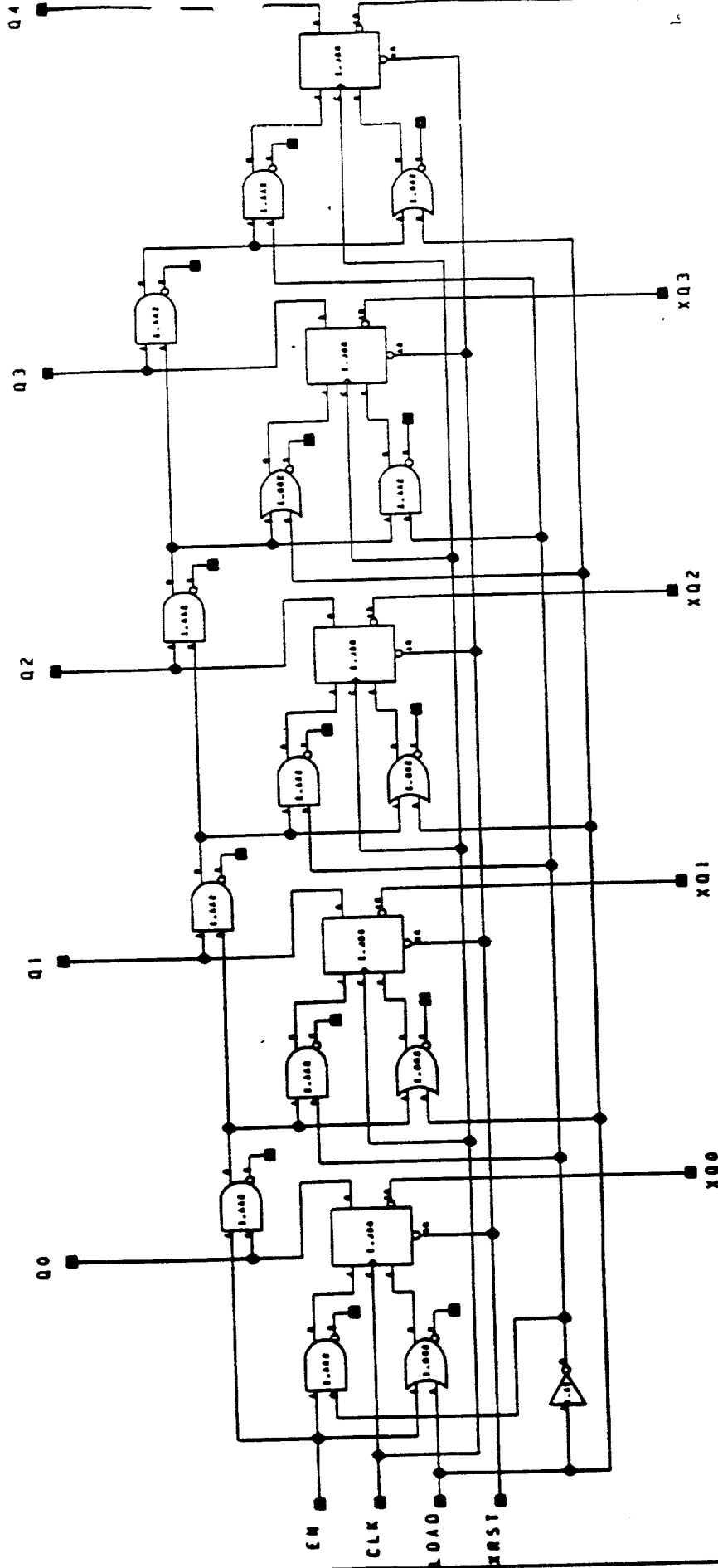


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TITLE F373.1
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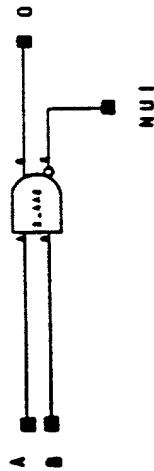
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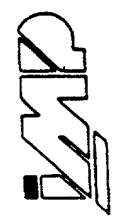
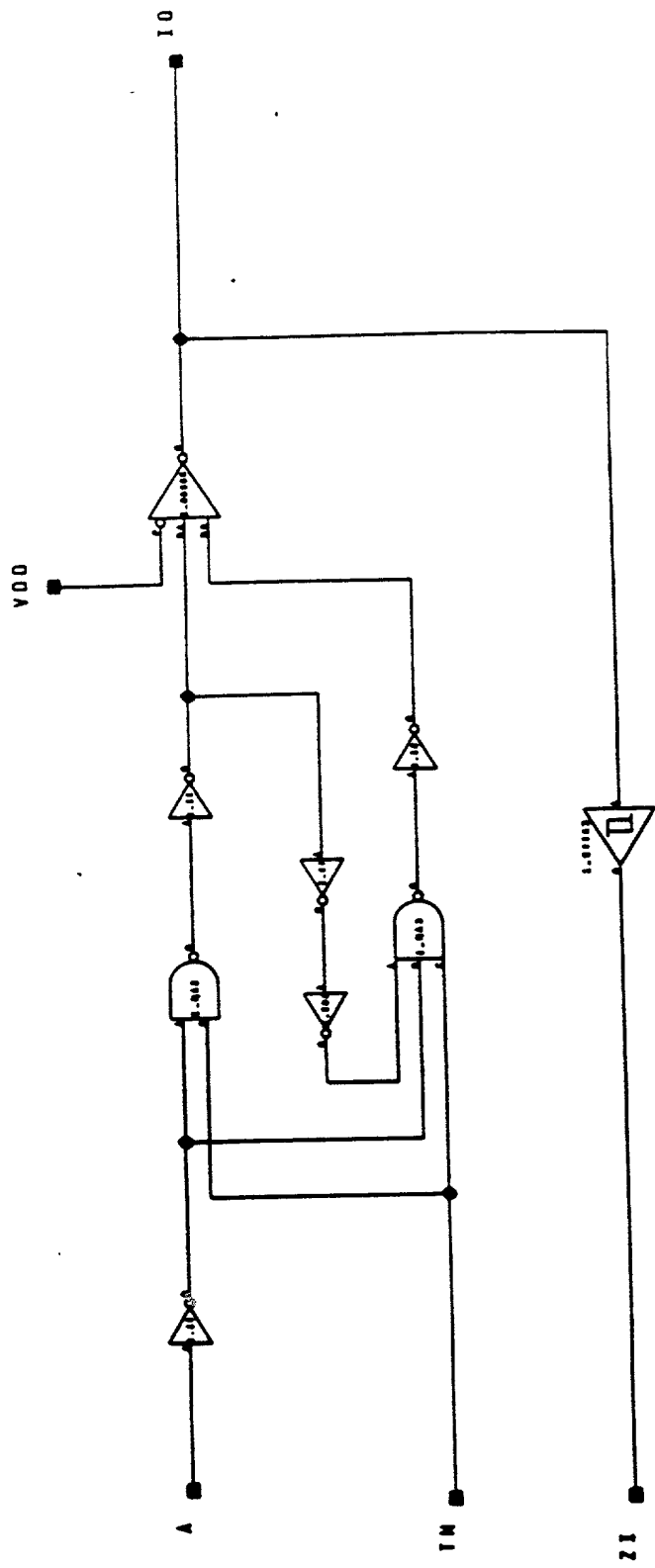


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 PAGE 58 OF 60
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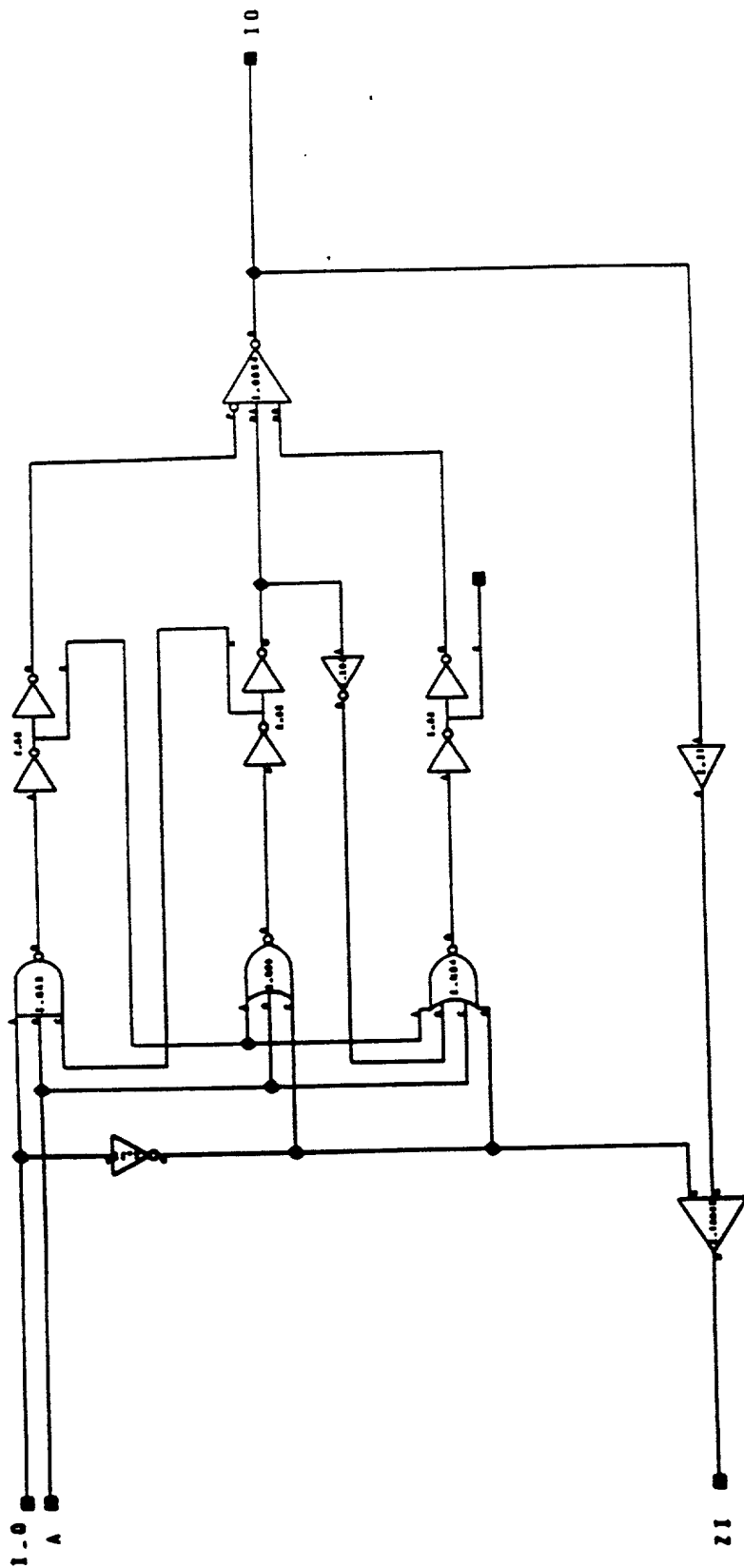
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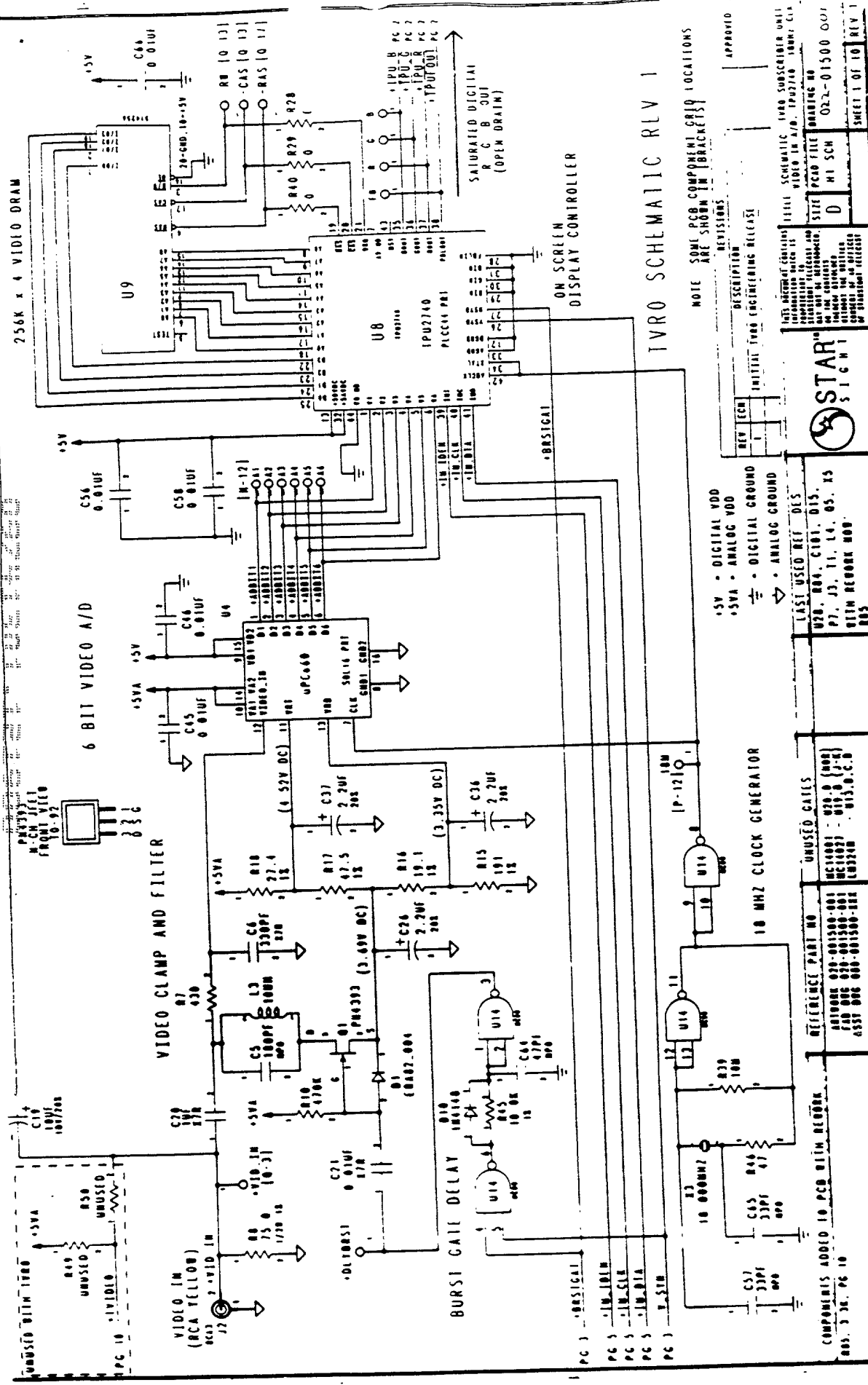
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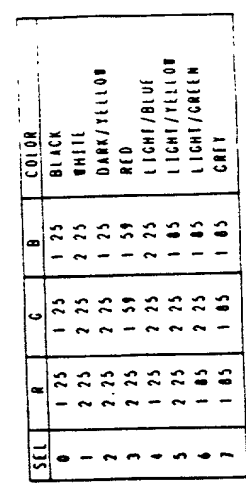


TITLE 1-002
 PAGE 60 OF 60
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SCHEMATIC DBE 1200
 P/N 10-01-00003



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BCA1
[C-12]

PHYSICAL

WARR VCO LOCUS 10 256 TIMES
MONITORIAL SYNC RATE
(256 X 15 734 26 KHZ - 4 027971 MHZ)
ADJUST C39 UNTIL BURST RATE - 15750 MHZ

COLOR BURST VCO LOCKS TO
4 TIMES THE COLOR BURST
(4.2359545 - 14.31010 MHz)

color burst filler

ANALOC R. C. B.
IN FROM COLOR CONVERTER


10K0 COLL 166MF-10264AC PIN OUT
SOME WIRINGS HAVE 71 TURNS

110

103-1A0

NUMBERS IN BRACKETS ARE PCB GRID LOCATIONS

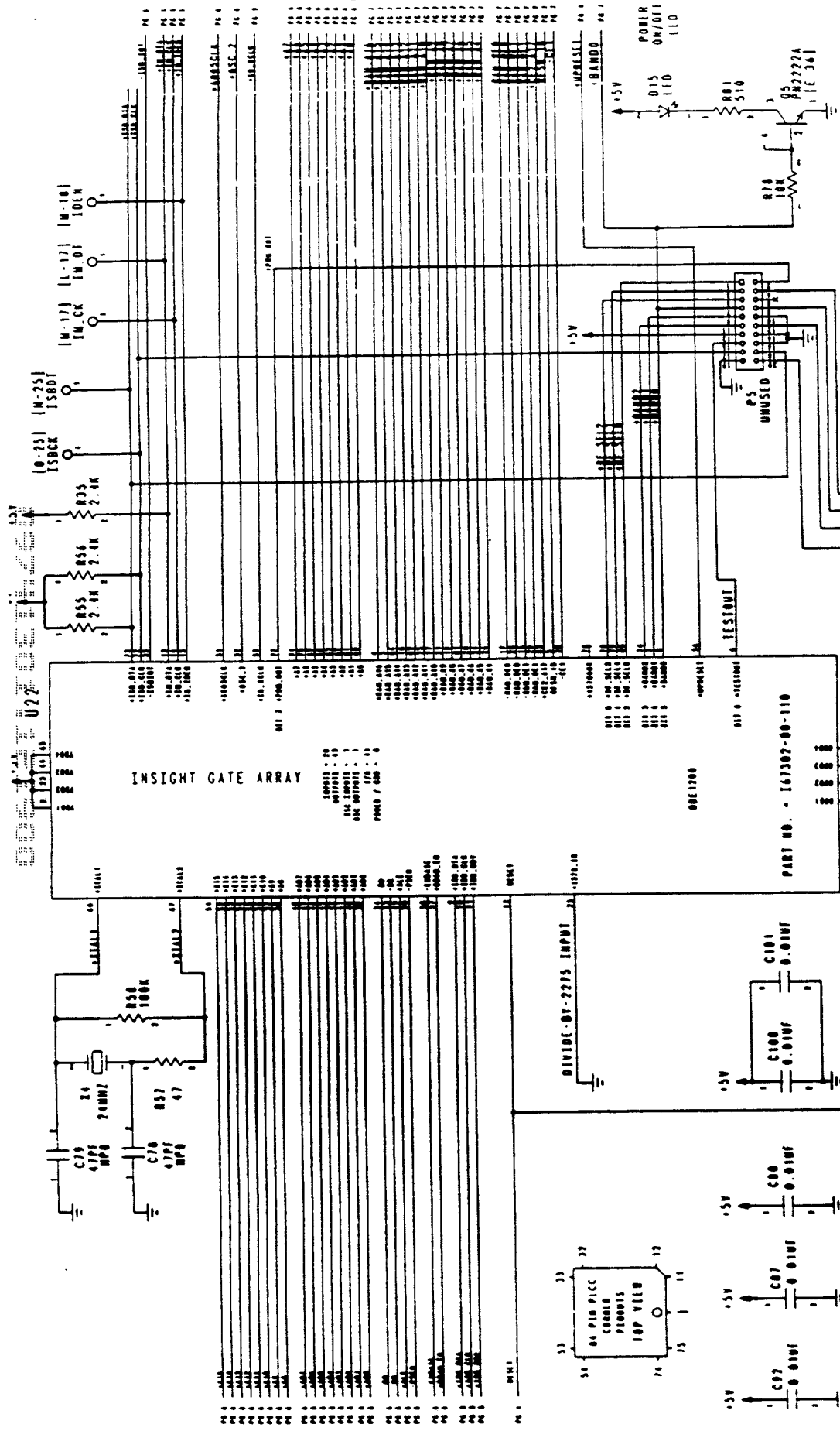
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HEREIN IS UNCLASSIFIED
DATE 04-11-2001 BY 60322
2. <u>EXCEPT WHERE SHOWN</u>
OTHERWISE, INFORMATION
CONTAINED HEREIN IS
UNCLASSIFIED
DATE 04-11-2001 BY 60322 | 3. <u>DATE OF REVIEW</u>
04-11-2001
4. <u>REVIEWER</u>
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60322 | 10. <u>REVIEWER'S PHONE NUMBER</u>
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04-11-2001 | 15. <u>REVIEWER'S LOCATION</u>
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60322 | 18. <u>REVIEWER'S LAST NAME</u>
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60322 | 27. <u>REVIEWER'S SECTION</u>
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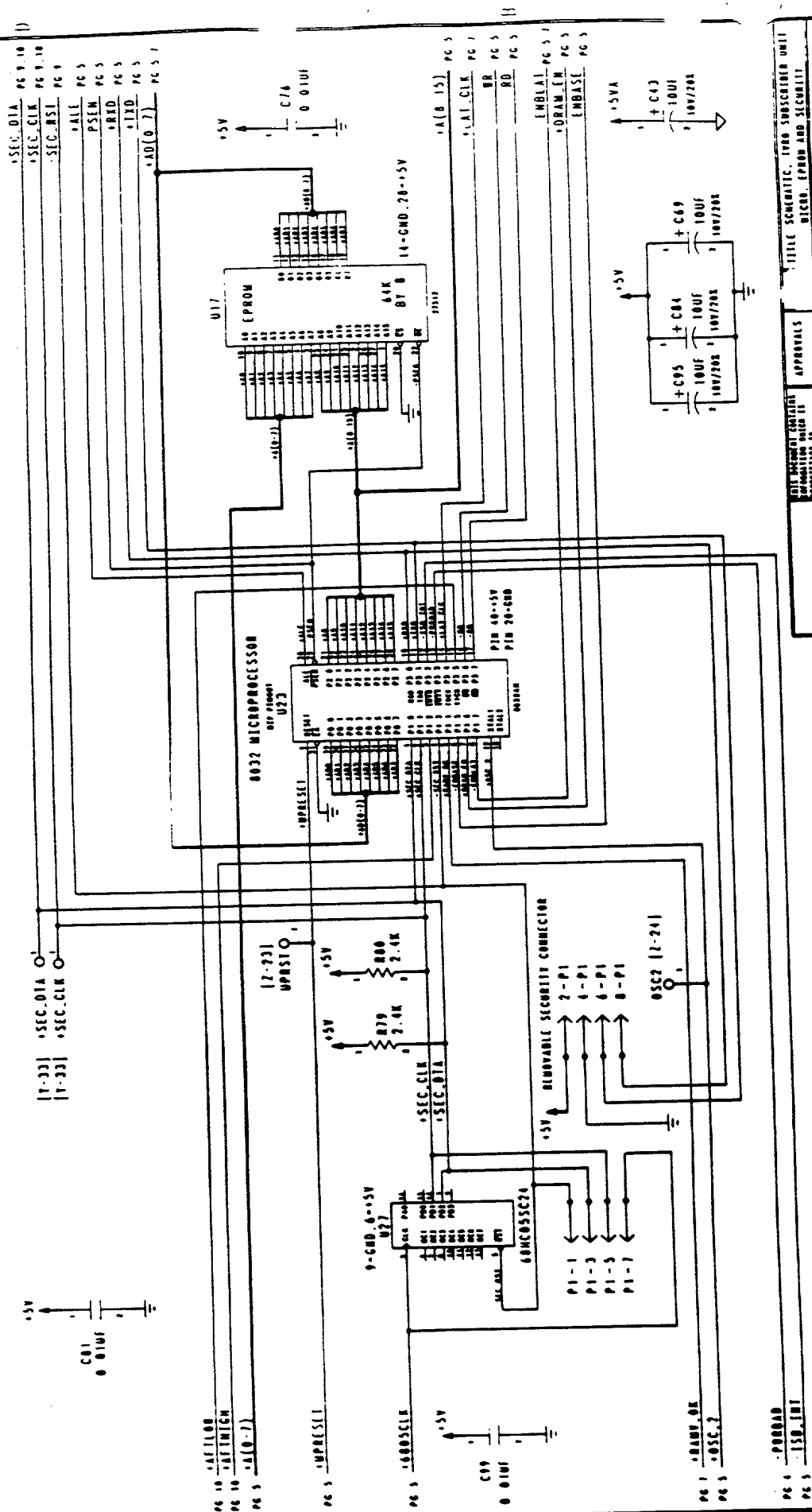
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LOCATION FOR SPL4301 DELAY LINE

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S I G N E T | | THIS DOCUMENT CONTAINS
INFORMATION WHICH IS
UNCLASSIFIED
DATE 01/01/01 BY 1040
EXEMPT FROM AUTOMATIC
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SCHEDULE 1, PARAGRAPH 1 | |
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# LOW VOLTAGE DETECTION LATCH

WHERE DOES IT CONTAIN PCR GRID LOCATIONS?

TABLE 1. *Brachycephalus* species with the highest number of individuals per species per site.

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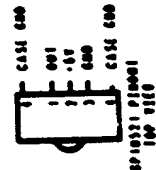
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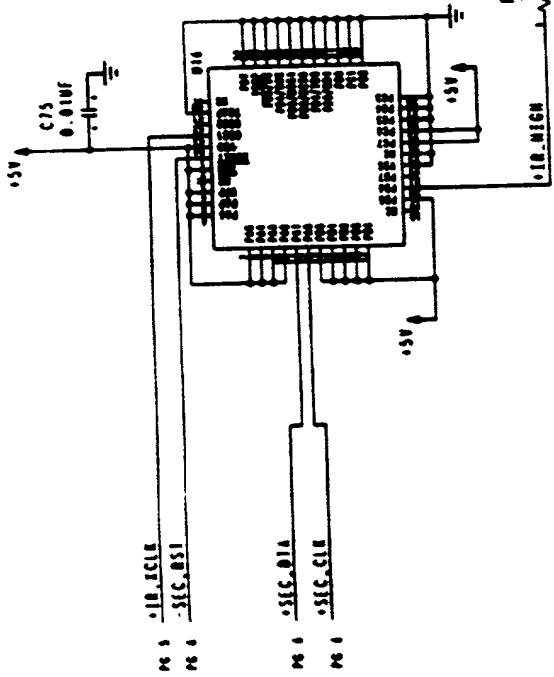
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|  <b>STAR<sup>®</sup></b><br>SCIENCE<br>SCINET | THIS DOCUMENT CONTAINS<br>INFORMATION THAT IS<br>UNCLASSIFIED, AND<br>IS NOT BEING<br>DECLASSIFIED<br>BECAUSE IT DOES NOT<br>MEET THE CRITERIA<br>FOR DECLASSIFICATION<br>PROGRAM OF AN AGENCY |  | TITLE SCHEMATIC, 1000 SUBSCRIBER UNIT<br>IN RECEIVER | DRAWING NO<br>032-01500-001 | SIZE READ FILE<br>D MB SCH | SHEET 8 OF 10 REV 1 |
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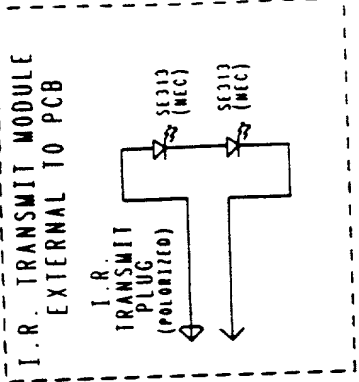
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# I. R TRANSMITTER

UNIVERSAL I. R. REMOTE TRANSMIT CHIP

I. R. DRIVER



NOTE: ALTERNATE I. R. DIODE - STANLEY AN304

|           |                     |                                                                   |                     |
|-----------|---------------------|-------------------------------------------------------------------|---------------------|
|           |                     | TITLE SCHEMATIC, IYBO SUBSCRIBER UNIT<br>I. R. REMOTE TRANSMITTER |                     |
| SIZE<br>D | PCB FILE<br>M9. SCH | DRAWING NO.<br>032-01500 001                                      | SHEET 9 OF 10 REV 1 |

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